

2020.06.15

Marking	Description
I	Install
NI	Non-install
PROTO	Proto-type
MP	Mass-Production
TAA	Install for TAA
CRITICAL	Critical Parts

21-OCT-2002		
DATE	CHANGE NO	REV

DESIGN / DRAWER	XXX	DATE	21-OCT-2002
CHECK	EXN LIN		
APPROVAL	MORRIS CHANG		
FILE NAME	CAMEL11A.6050A3167801.MB.001		
PCB P/N	60XXXXXXX0000X	PCB VER	XXX

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION MAIN BOARD			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET 1 of 100			

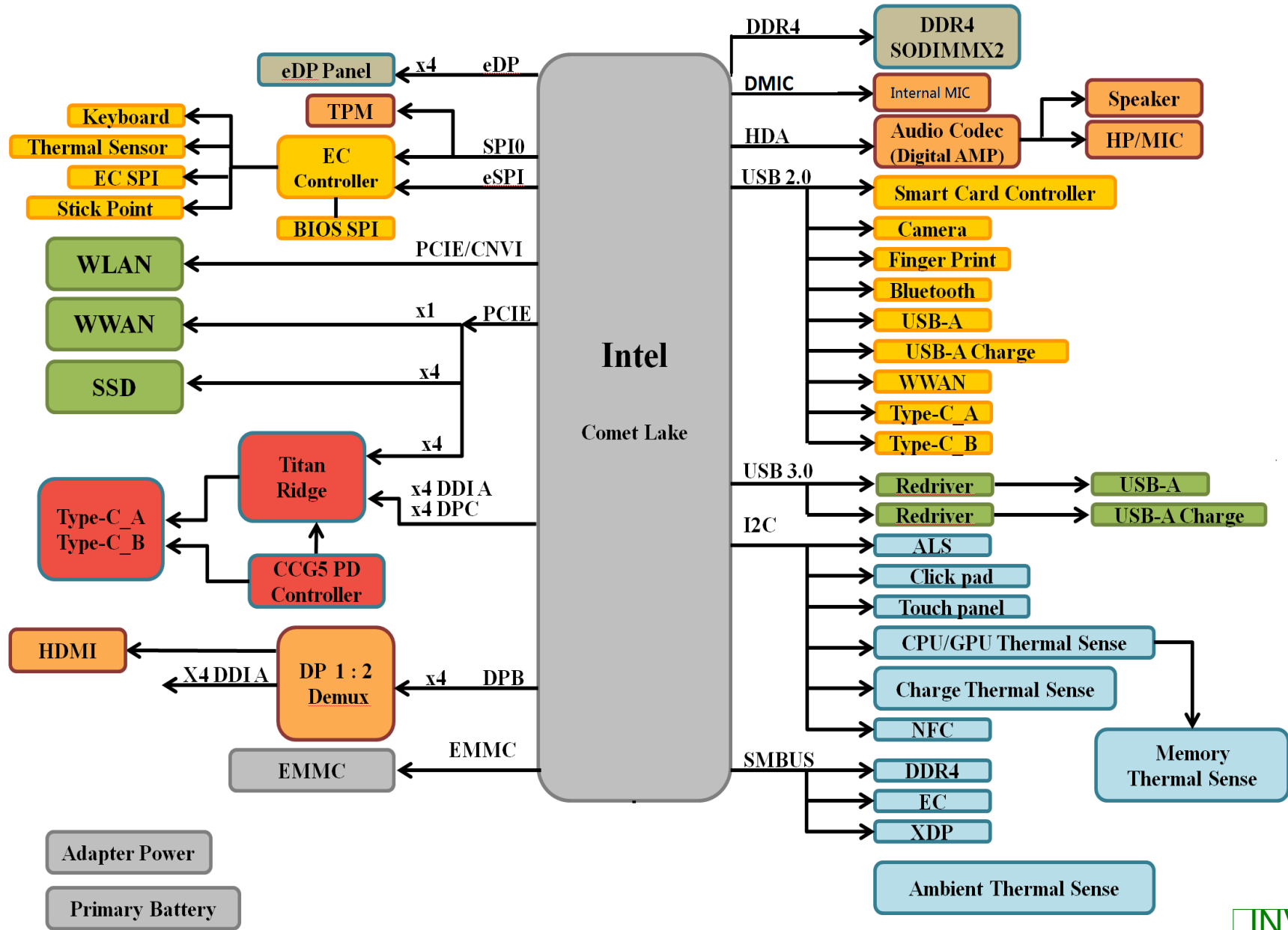
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INVENTEC

TITLE MODEL PROJECT,FUNCTION
TABLE OF THE CONTENT

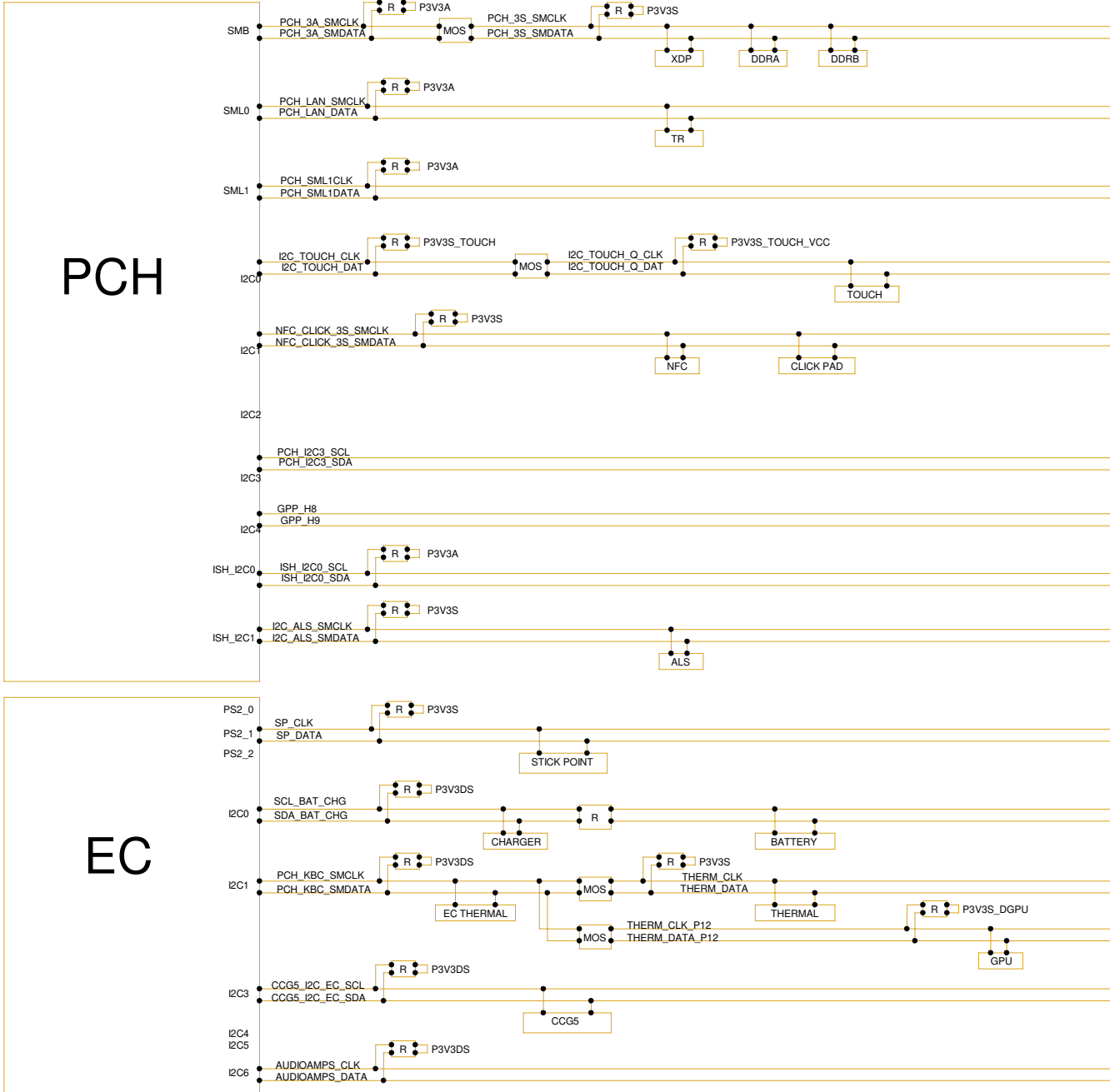
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100% TO 45%

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BLOCK Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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SIGNAL	VOLTAGE	G3	S5	S3	S0
RTC	3.3V	O	O	O	O
PVBAT	12V	X	O	O	O
P3V3DS	3.3V	X	O	O	O
P3V3A	3.3V	X	O	O	O
P3V3S	3.3V	X	X	X	O
P5V0DS	5V	X	O	O	O
P5V0A_USB	5V	X	O	O	O
P5V0S	5V	X	X	X	O
P1V05A	1.05V	X	O	O	O
P1V2	1.2V	X	X	O	O
P2V5	3.3V	X	X	O	O
P1V8DS	1.8V	X	O	O	O
P1V5S	1.5V	X	X	X	O
PVCCIO	0.95V	X	X	X	O
PVCC_PRIM	1.05V	X	O	O	O
PVCORE	SVID	X	X	X	O
PVCCGT	SVID	X	X	X	O
PVCCSA	1V	X	X	X	O
PVCCSFR	1.05V	X	X	O	O
PVCORE_DGPU	SVID	X	X	X	O
P1V35S_DGPU	1.35V	X	X	X	O
PVDDCL_DGPU	SVID	X	X	X	O
P1V8S_DGPU	1.8V	X	X	X	O

STACK_UP for 8 layer-0.9mm-5Signal

Top		0.60mil	(Solder Mask)	1 OZ min
	P/P 2.7mil	1.55mil	(Hoz + Plating)	
L2		2.70mil		
	CORE 4mil	0.60mil	(GND)	H OZ
		4.00mil		
L3				
	P/P 4.4mil	0.60mil	(SIGNAL)	H OZ
		4.40mil		
L4				
	CORE4mil	1.30mil	(VCC)	1 OZ
		4.00mil		
L5				
	P/P 4.4mil	1.30mil	(SIGNAL)	1 OZ
		4.40mil		
L6				
	CORE 4mil	0.60mil	(SIGNAL)	H OZ
		4.00mil		
L7				
	P/P 2.7mil	0.60mil	(GND)	H OZ
		2.70mil		
Bottom		1.55mil	(Hoz + Plating)	1 OZ min
		0.60mil	(Solder Mask)	

ideal: 35.50mil
0.90mm

Premium-U Port	Base-U Port	Oleander 13" x360/Camellia 13/14" Base-U
USB30_P1/PCle1	USB30_P1	USB Type A(Left)
USB30_P2/PCle2	USB30_P2	USB Type A(Left)
USB30_P3/PCle3	USB30_P3	Free
USB30_P4/PCle4	USB30_P4	Free
USB30_P5/PCle5	PCle5	TBT
USB30_P6/PCle6	PCle6	TBT
PCle7(GbE)	PCle7(GbE)	TBT
PCle8(GbE)	PCle8(GbE)	TBT
PCle9 (GbE)	PCle9 (GbE)	SSD(NVMe)
PCle10	PCle10	SSD(NVMe)
PCle11 /SATA 0	PCle11 /SATA 0	SSD(NVMe)
PCle12 /SATA 1A	PCle12 /SATA 1A	SSD(NVMe)/SATA SSD
PCle13 (GbE)	PCle13 (GbE)	Free
PCle14 (GbE)	PCle14 (GbE)	WLAN
PCle15 /SATA 1B	PCle11	Free
PCle16 /SATA 2	PCle12	WWAN(PCIE)

DDI	Premium-U Port	Base-U Port	eDP
0-eDP	DDI PortA	DDI PortA	eDP
1	DDI PortB	DDI PortB	Demux to TR DDI2/HDMI
2	DDI PortC	DDI PortC	TR DDI1
USB2			
1	USB 2.0 #1	USB 2.0 #1	USB Type A(Right)
2	USB 2.0 #2	USB 2.0 #2	Camera
3	USB 2.0 #3	USB 2.0 #3	Smart Card
4	USB 2.0 #4	USB 2.0 #4	USB Type A(Left)
5	USB 2.0 #5	USB 2.0 #5	Type C2
6	USB 2.0 #6	USB 2.0 #6	Type C1
7	USB 2.0 #7	USB 2.0 #7	Finger Print Reader
8	USB 2.0 #8	NA	
9	USB 2.0 #9	NA	WWAN (USB2.0)
10	USB 2.0 #10 (CNVI enable platform, this port need to for BT port.)	USB 2.0 #10 (CNVI enable platform, this port need to for BT port.)	Bluetooth

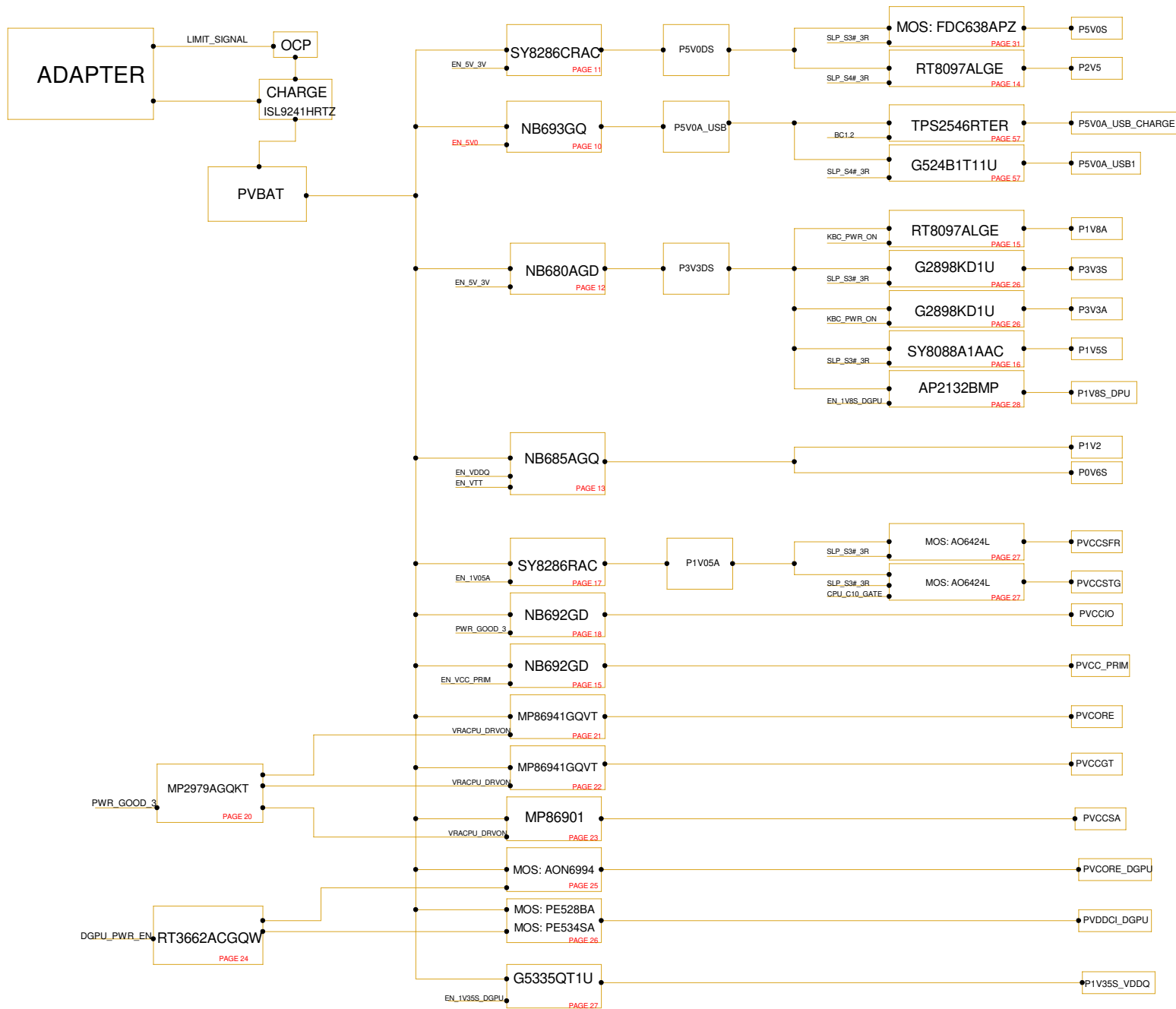
	PCH GPIO				
	BRD_ID1	BRD_ID2	BRD_ID3	BRD_ID4	
	GPP_H12	GPP_H13	GPP_H14	GPP_H15	
CPU Socket Board / Reserved	0	0	0	0	0
FIP board / Reserved	1	0	0	0	1
DB1	2	0	0	1	0
SI1	3	0	0	1	1
SI1R	4	0	1	0	0
	5	0	1	0	1
SI2	6	0	1	1	0
SI2R	7	0	1	1	1
	8	1	0	0	0
SI3	9	1	0	0	1
	10	1	0	1	0
PV	11	1	0	1	1
PVR	12	1	1	0	0
	13	1	1	0	1
MV	14	1	1	1	0
	15	1	1	1	1

BIOS Group 2				
PCH PLT_ID				
0: P(CMIT) ; 1: W(mWS)		0:14" ; 1:15"		BIOS Group 2
				SG_IN ; UMA-0 ; DSC -1
PLT_ID2	PLT_ID3	PLT_ID4	SG_IN	
GPP_F9	GPP_F10	GPP_A16	GPP_F3	
Camellia 15W UMA	1	1	1	0
Camellia 15W DSC	1	1	1	1
Camellia 15P UMA	0	1	1	0
Camellia 15P DSC	0	1	1	1
Camellia 14W DSC	1	0	1	1

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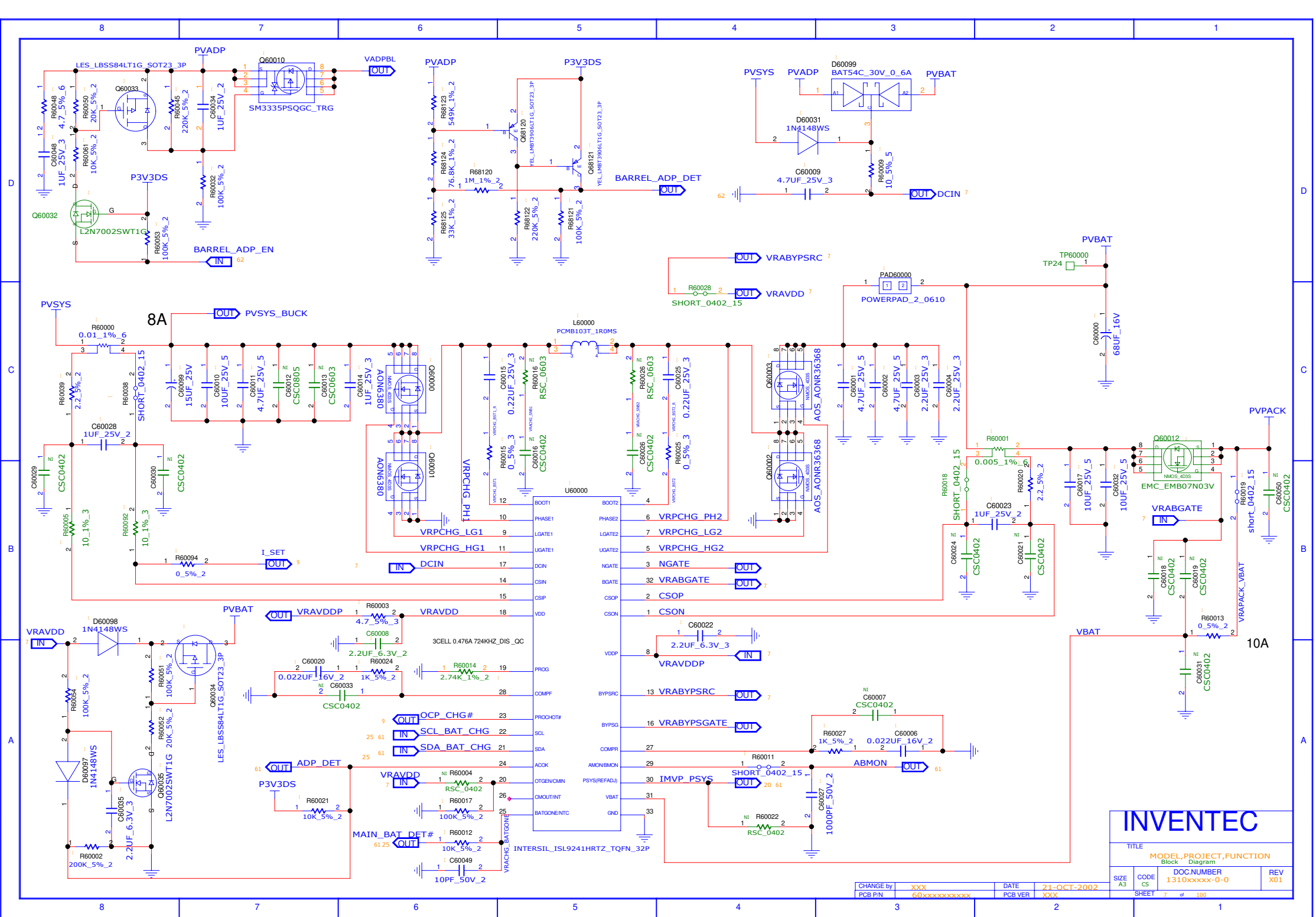
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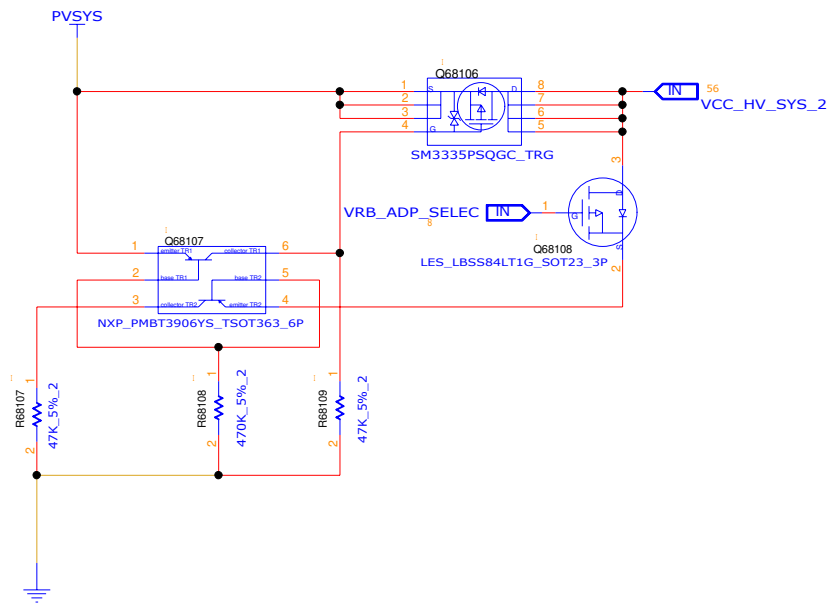
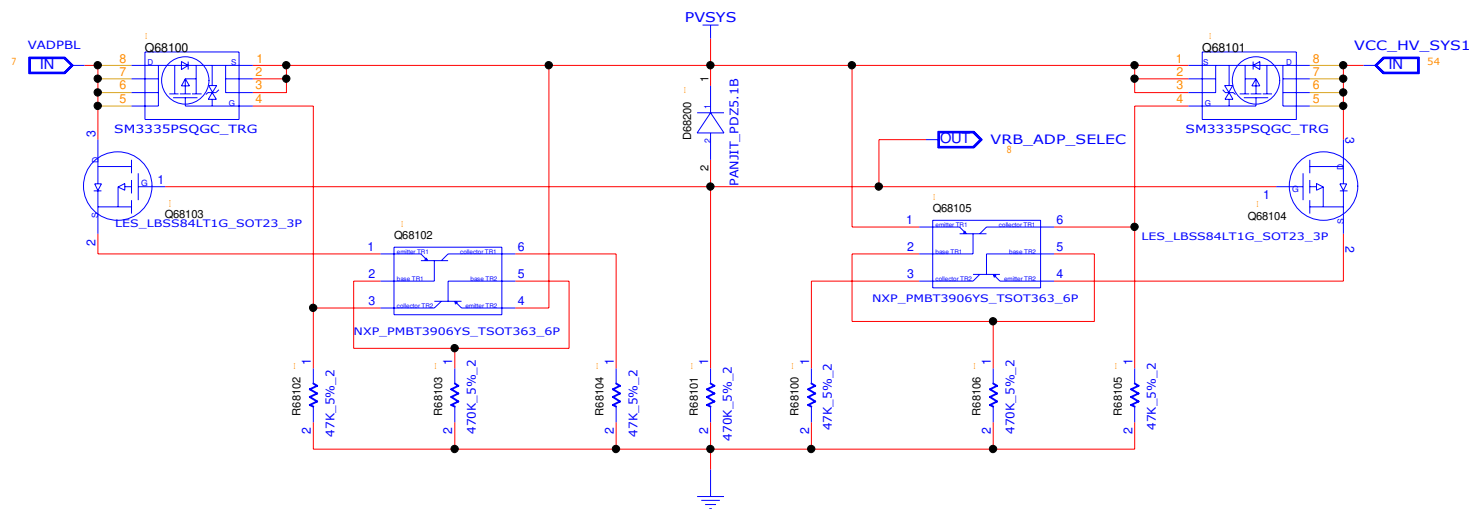
CHGNGN	xxx	DATE	21-OCT-2002
PCB PN	60xxxxxxxxxx	PCB VER	xxx



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
POWER BLOCK DIAGRAM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310XXXX-0-0	201
SHEET	#	#	USE
1	1		

CHANGES	DATE	21-OCT-2002
PCB PIN	PCB VER	XXXX





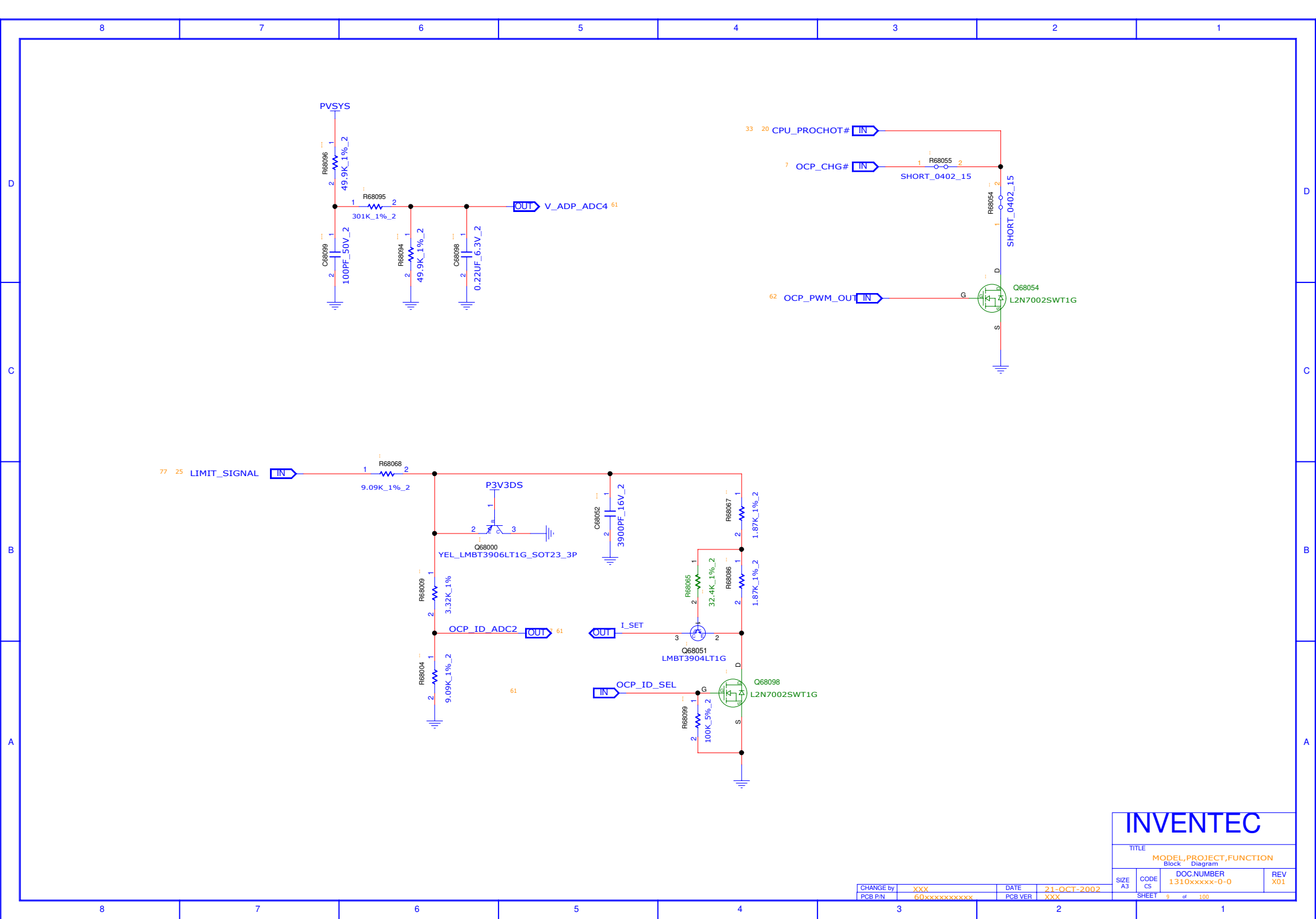
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TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

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CHANGE by XXX DATE 21-OCT-2002
PCB P/N 60xxxxxxxxxxx PCB VER XXX

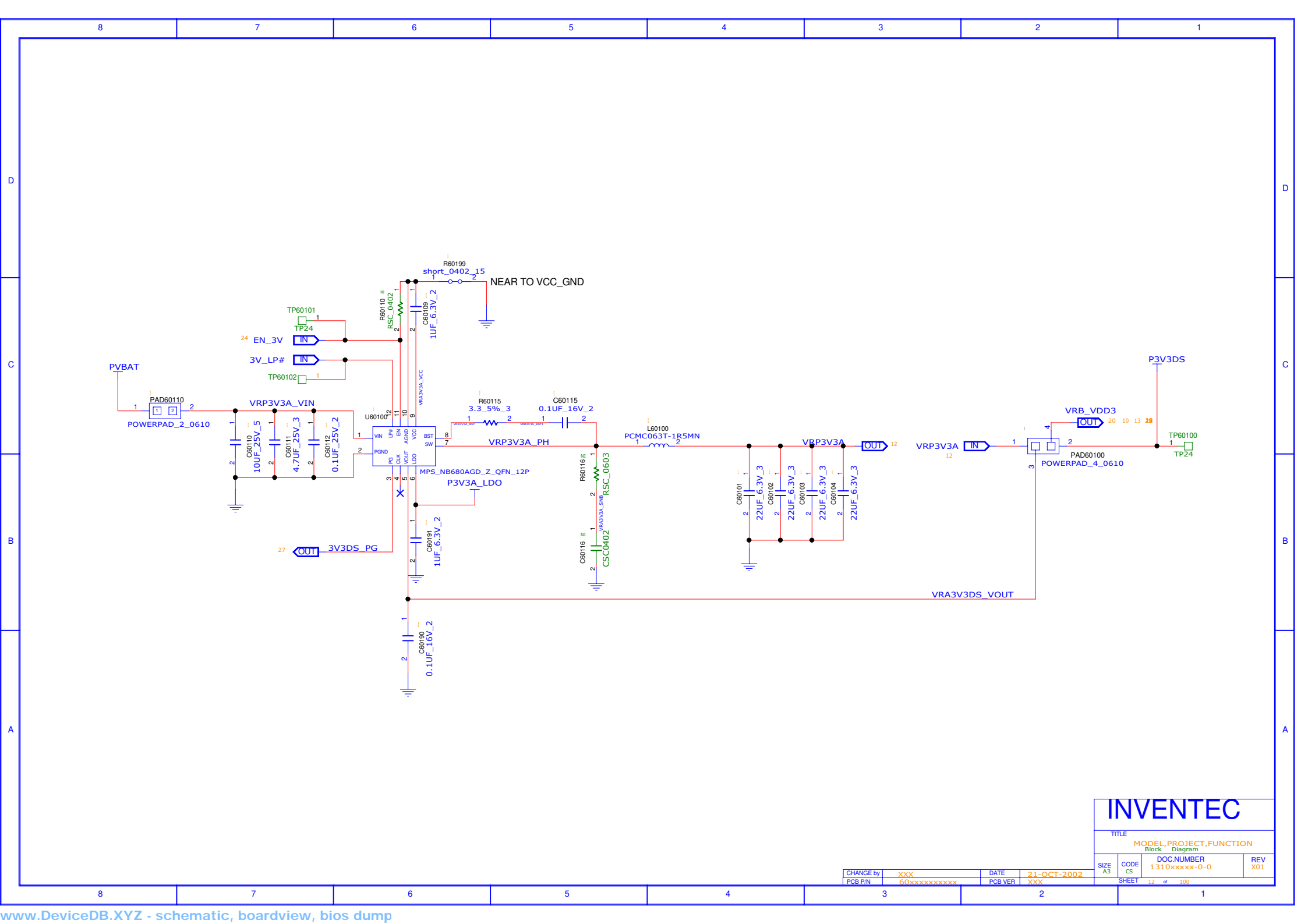


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TITLE
MODEL, PROJECT, FUNCTION
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INVENTEC

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Block Diagram

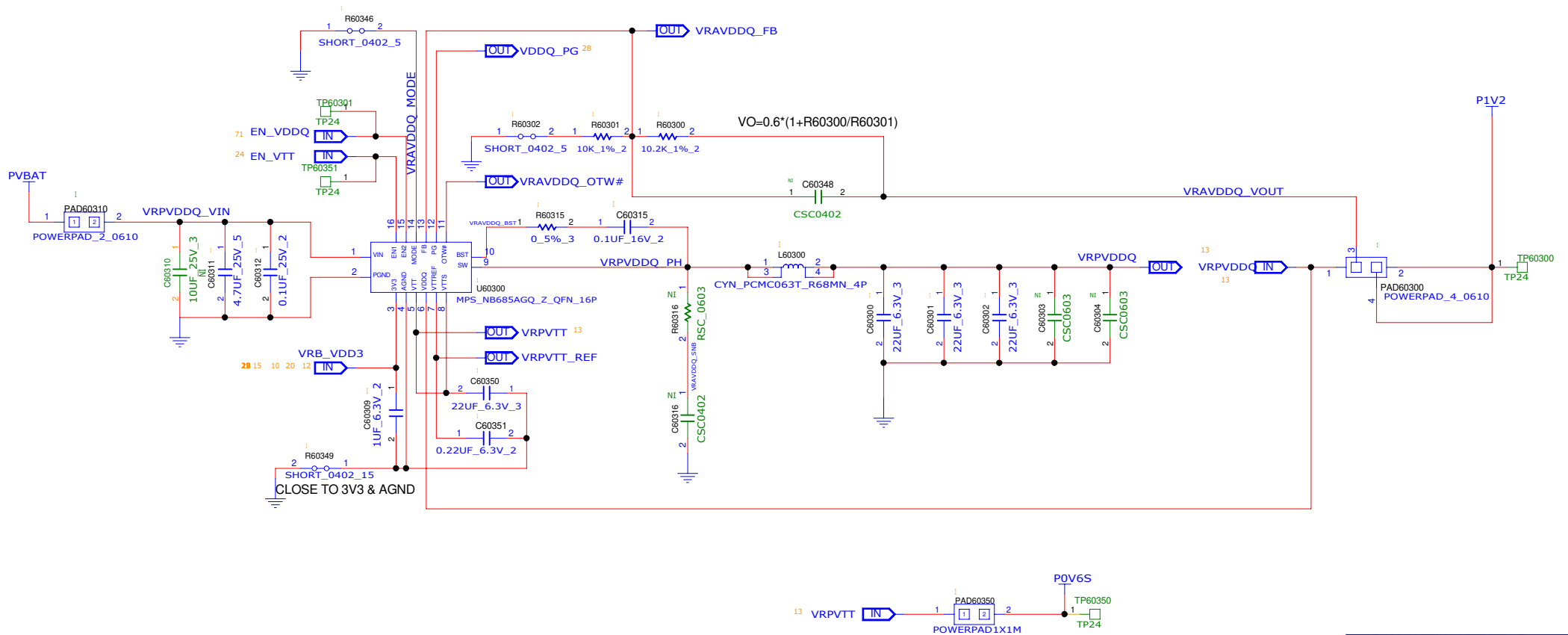
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Table 1—EN1/EN2 Control

State	EN1	EN2	VDDQ	VTTREF	VTT
S0	High	High	ON	ON	ON
S3	Low	High	ON	ON	OFF(High-Z)
S4/S5	Low	Low	OFF	OFF	OFF
Others	High	Low	OFF	OFF	OFF



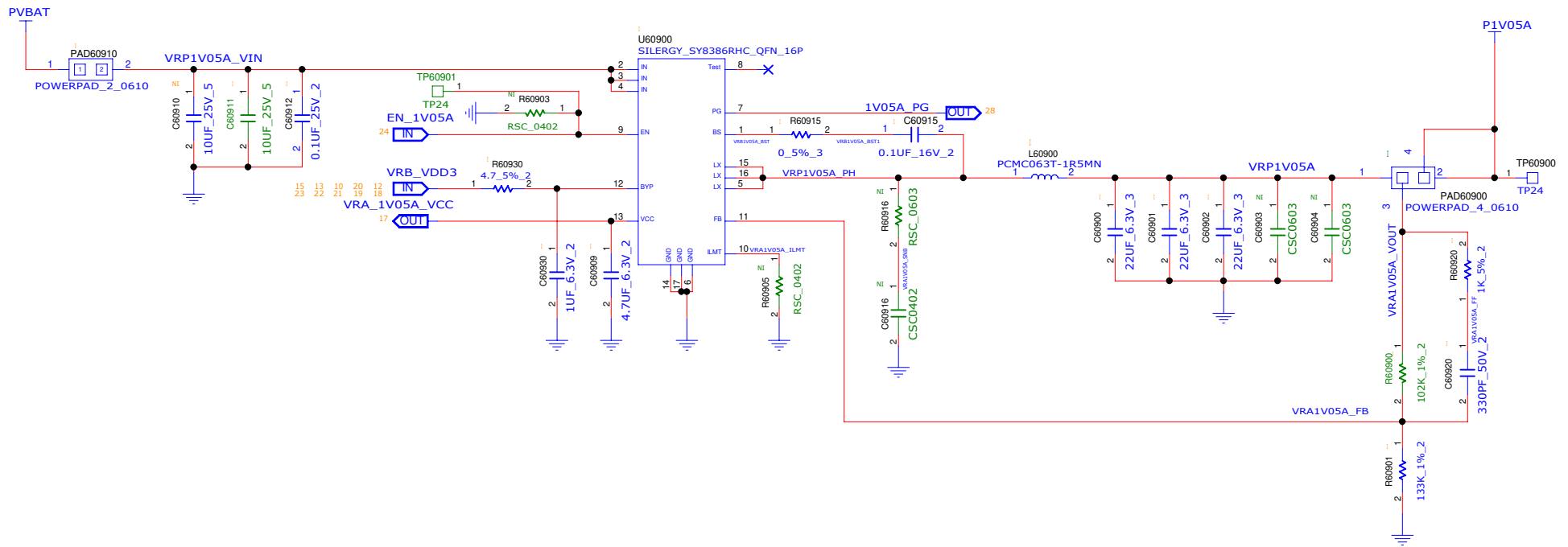
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TITLE

MODEL, PROJECT, FUNCTION

Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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$$VO=0.6*(1+R60900/R60901)$$

INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

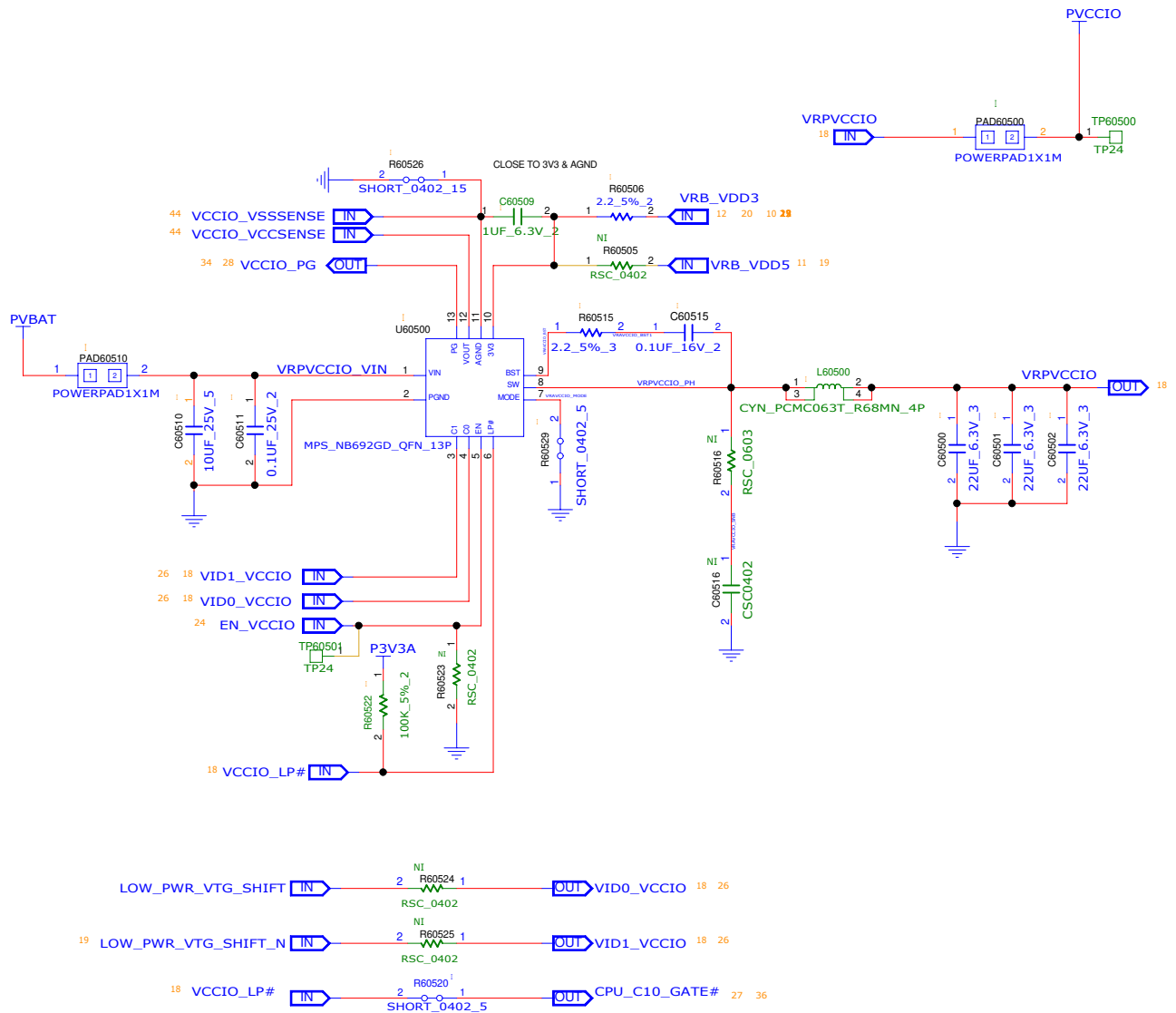
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MODE	VR Rail	Resistor to GND (1% Accuracy)
M1	VCCIO	0
M2	PRIMCORE	Float or > 230K
M3	EDRAM/V1.0A/EOPIO	100K
M4	Others	150K

	LP#	C1	C0	VOUT(V)
VCCIO	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
VCCPRIM CORE	0	X	X	0.75
	1	0	0	0.9
	1	0	1	0.95
	1	1	0	1.0
	1	1	1	1.05
EDRAM/ EOPIO/ V1.0A	0	X	X	0
	1	0	0	0.8 (MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
Others (Fixed design only, not allowed for changing on-the-fly)	0	0	0	1.59
	0	0	1	1.99
	0	1	0	2.38
	0	1	1	3.3
	1	0	0	1.2
	1	0	1	1.5
	1	1	0	1.8
	1	1	1	2.5

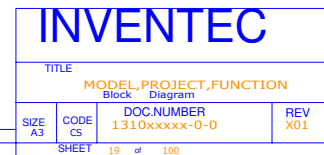


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TITLE			
MODEL,PROJECT,FUNCTION			
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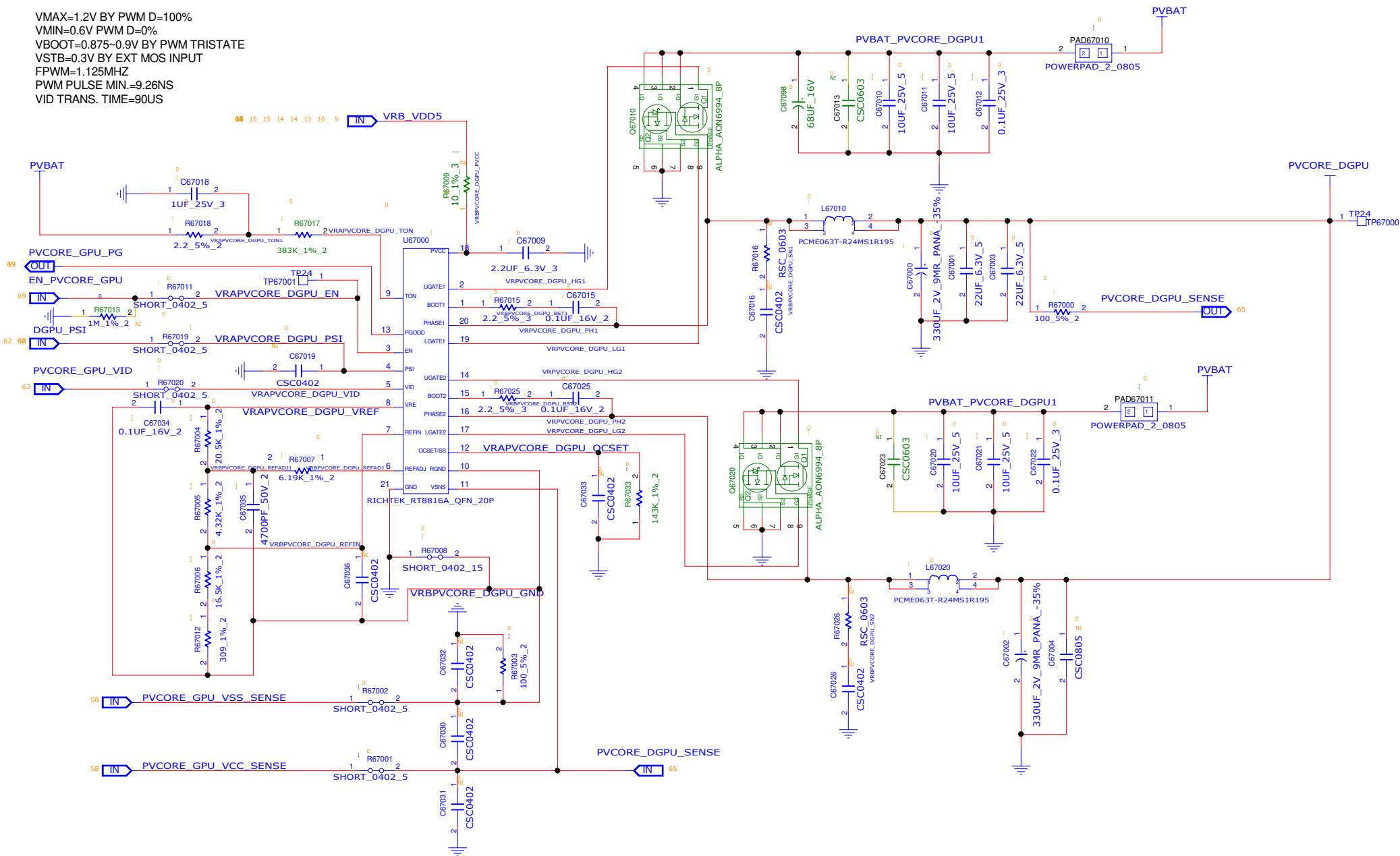
	LP#	C1	C0	VOUT(V)
VCCIO	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
VCCPRIM _CORE	0	X	X	0.75
	1	0	0	0.9
	1	0	1	0.95
	1	1	0	1.0
	1	1	1	1.05
EDRAM/ EOPIO/ V1.0A	0	X	X	0
	1	0	0	0.8 (MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
Others (Fixed design only, not allowed for changing on-the-fly)	0	0	0	1.59
	0	0	1	1.99
	0	1	0	2.38
	0	1	1	3.3
	1	0	0	1.2
	1	0	1	1.5
	1	1	0	1.8
	1	1	1	2.5







VMAX=1.2V BY PWM D=100%
 VMIN=0.6V PWM D=0%
 VBOOT=0.875-0.9V BY PWM TRISTATE
 VSTB=0.3V BY EXT MOS INPUT
 FPWM=1.125MHZ
 PWM PULSE MIN.=9.26NS
 VID TRANS. TIME=90US

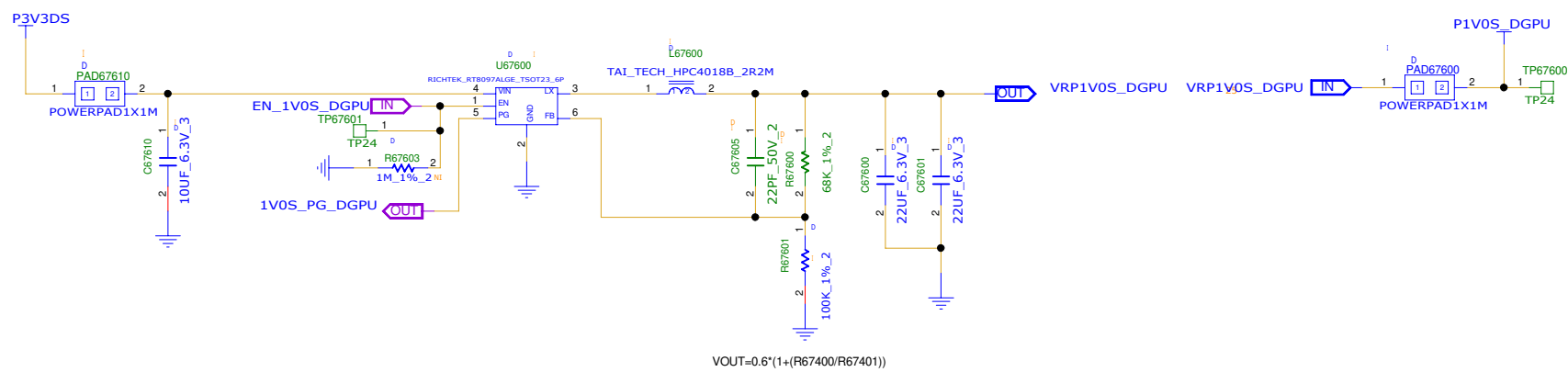


dGPU core BTO	U67000	R67017	C67040	R60740	R60741	R60742	R67033	R67004	R67005	R67006	R67012	C67035	R67007
N17S-G1	RT8816A	383K_1%	NI	0_5%	NI	NI	137K_1%	20.5K_1%	4.32K_1%	16.5K_1%	309_1%	4700pF	6.19K_1%

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MODEL PROJECT,FUNCTION			
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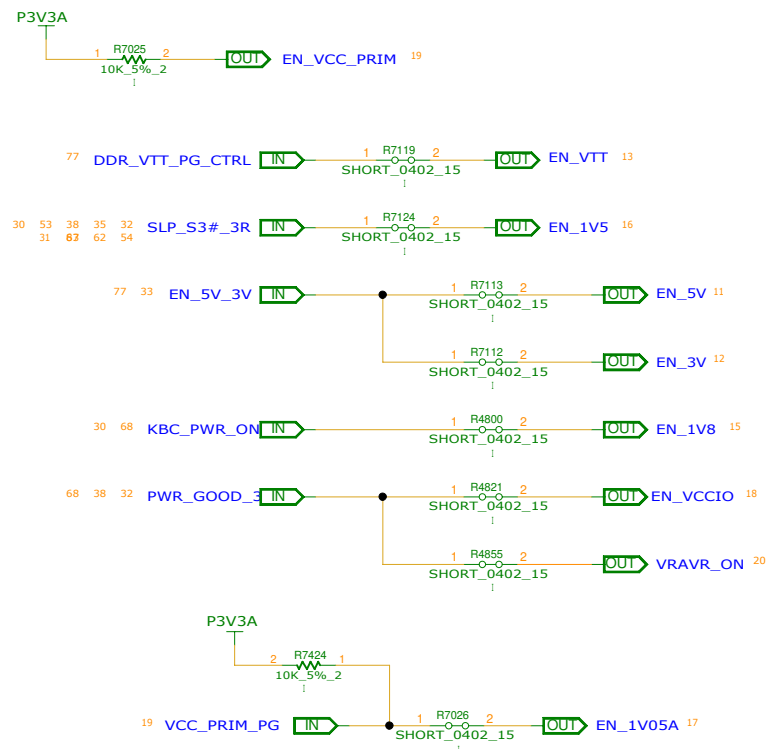


TDC=0.8A
OCP=3.2A

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POWER ENABLE SIGNAL

POWER TO EE NET NAME CONNECTION



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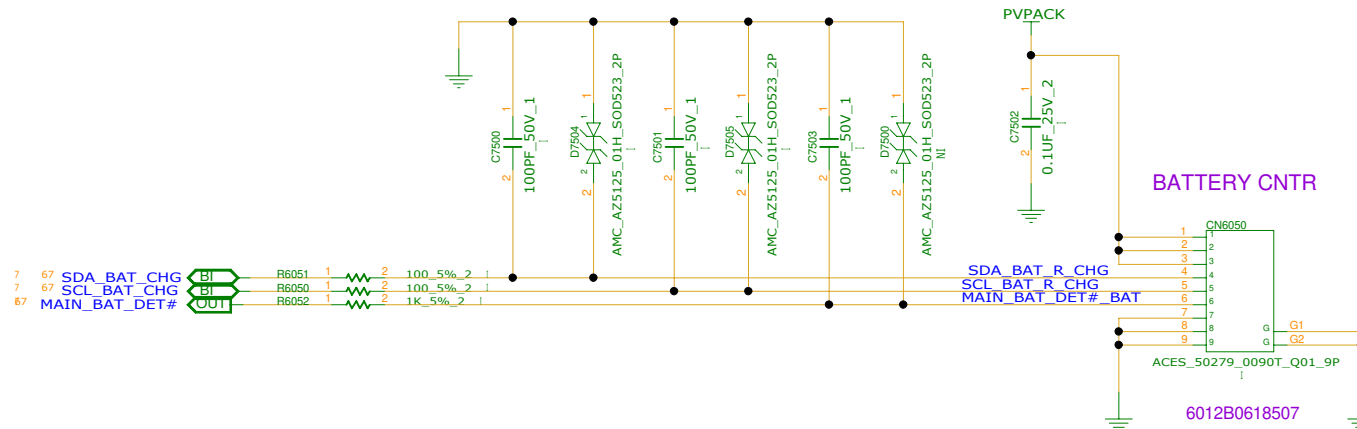
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TITLE
MODEL PROJECT FUNCTION
POWER ENABLE SIGNALS

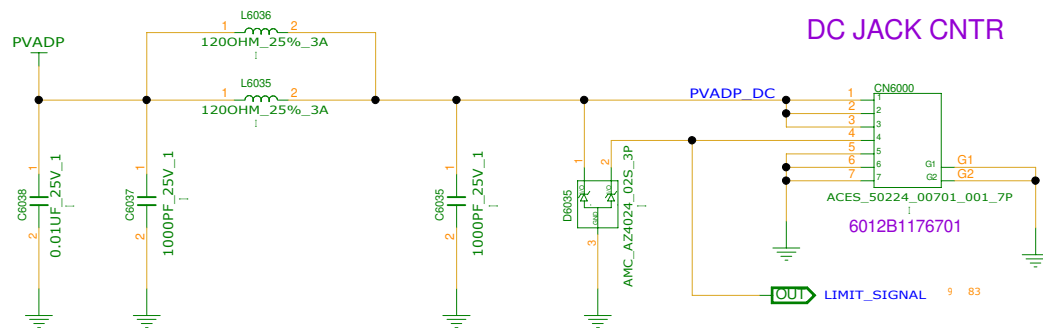
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LOCATION NUMER : 6050~6099



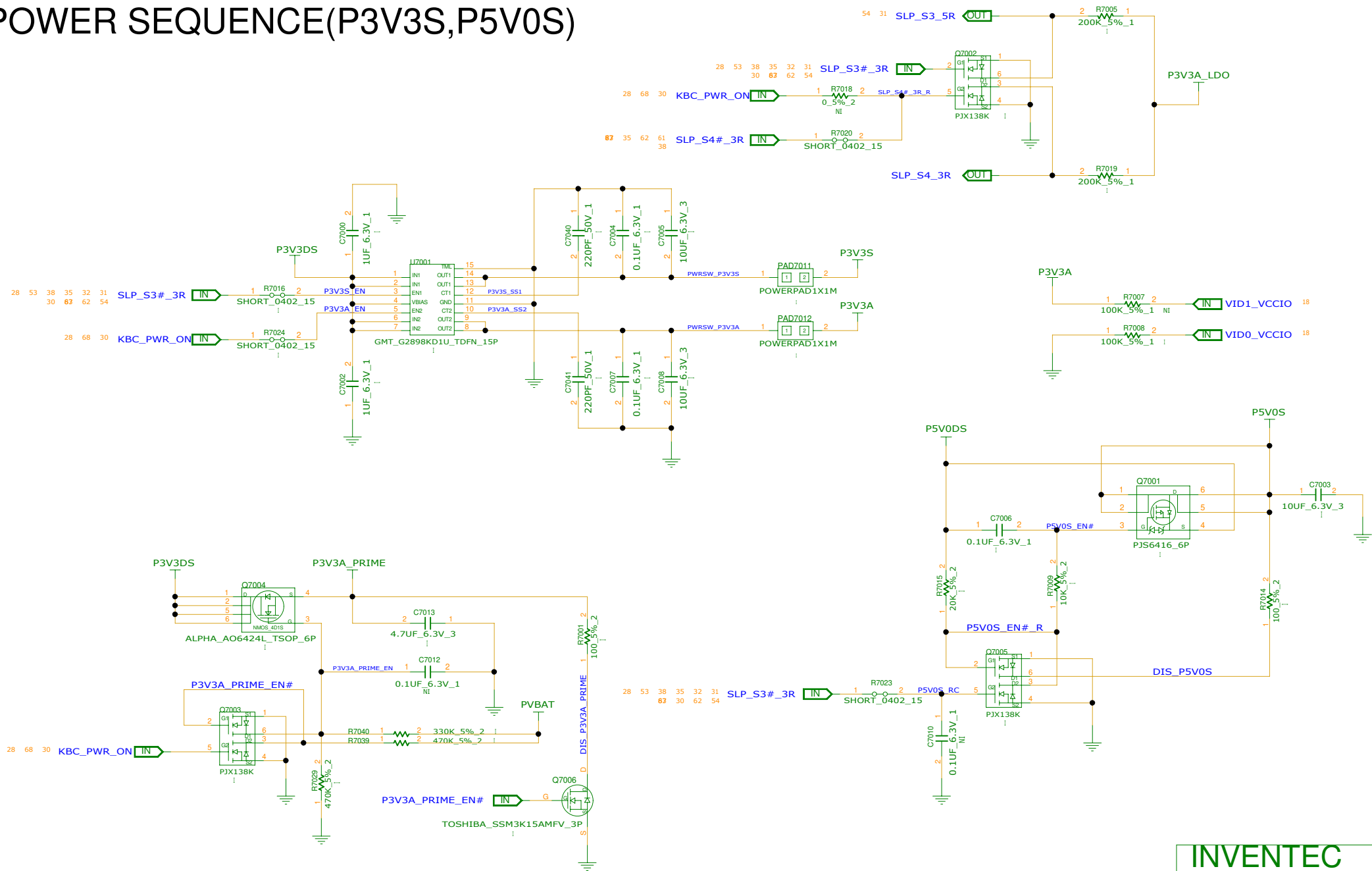
LOCATION NUMER : 6000~6049



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
DC JACK & BATT CNTR			
SIZE	CODE	DOC NUMBER	REV
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POWER SEQUENCE(P3V3S,P5V0S)



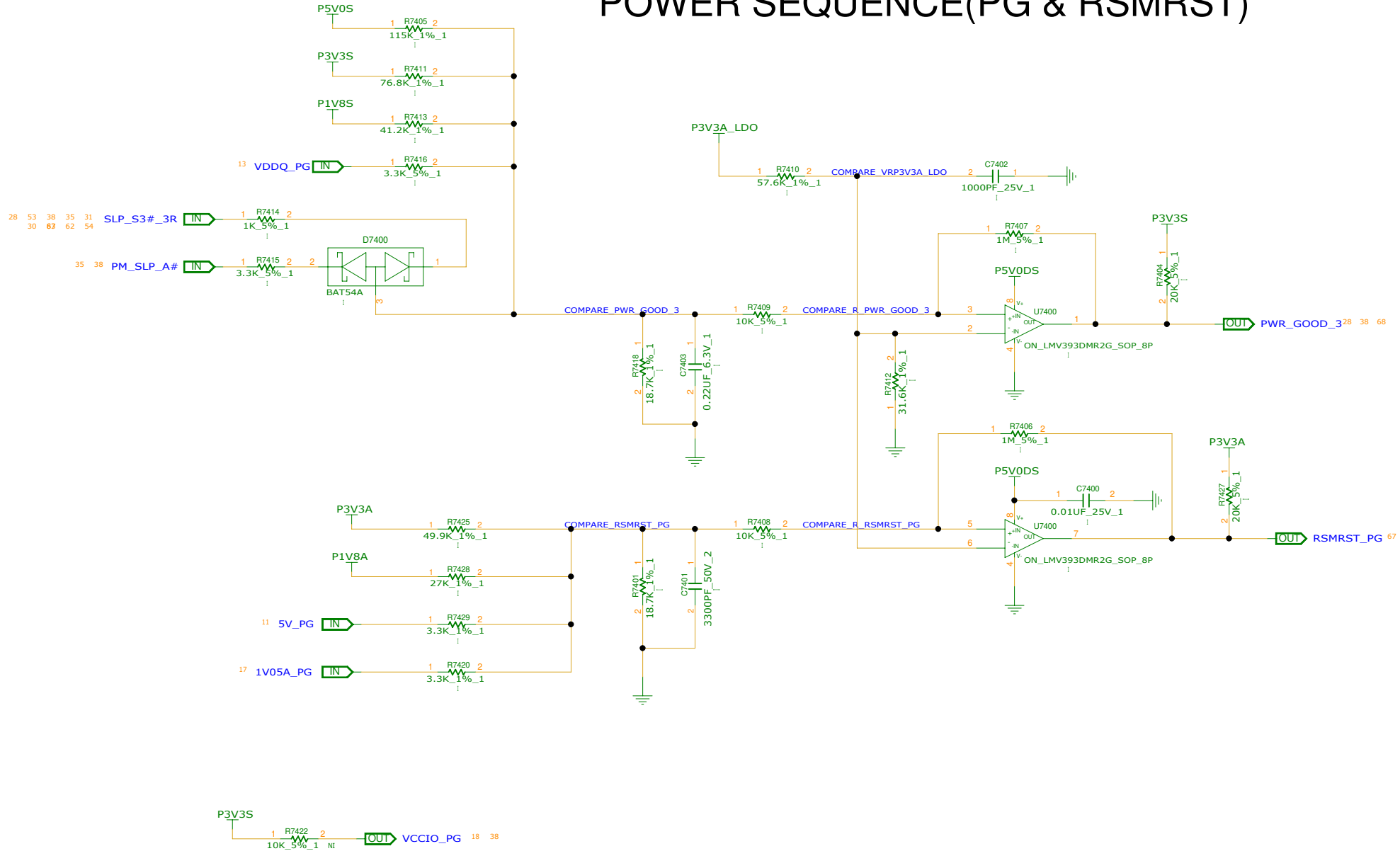
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INVENTEC

TITLE
MODEL PROJECT,FUNCTION
POWER SEQUENCE (P3V3,P5V0)

POWER SEQUENCE(PG & RSMRST)



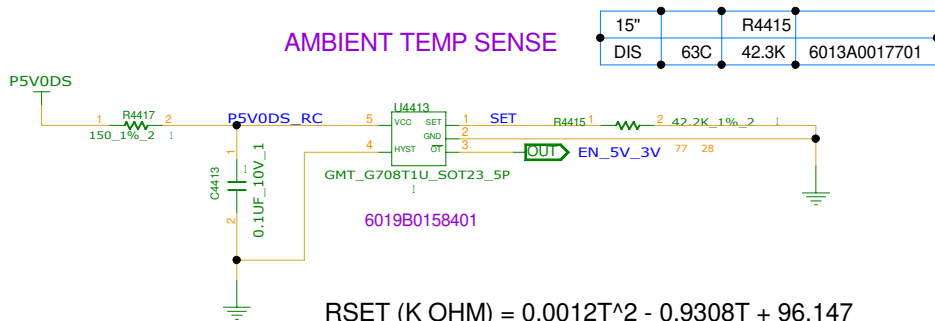
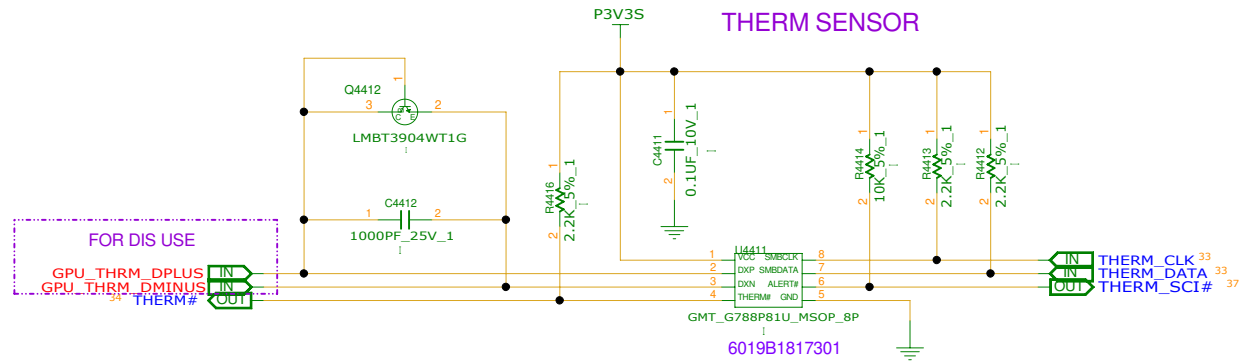
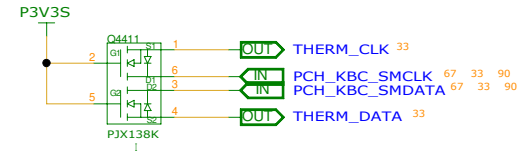
LOCATION NUMER : 7000~7499

INVENTEC

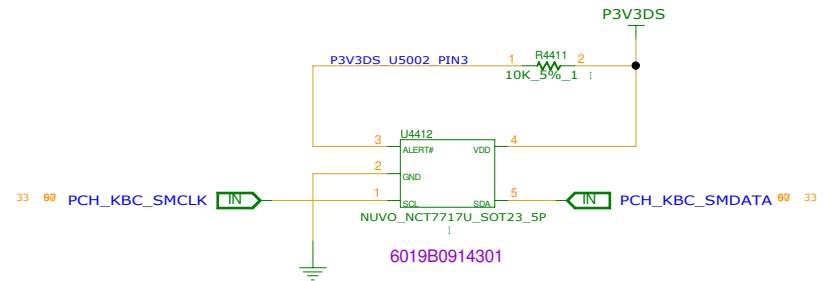
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MODEL PROJECT FUNCTION POWER SEQUENCE(PG & RSMRST)			
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THERMAL



RSET (K OHM) = $0.0012T^2 - 0.9308T + 96.147$
THERMAL SHUTDOWN AT 75 DEG.



LOCATION NUMBER:4411~4449

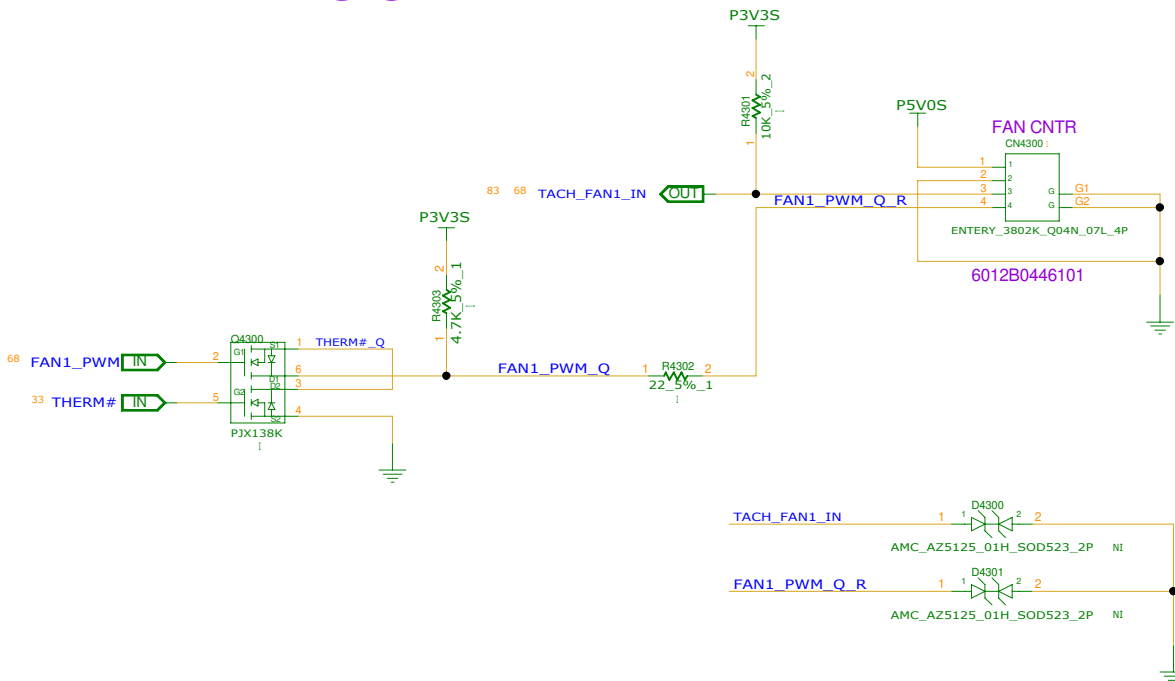
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PCB P/N	60xxxxxxxxxx	PCB VER	XXX	SHEET	33 of 100		

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TITLE	MODEL,PROJECT,FUNCTION
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SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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FAN CONN



LOCATION NUMBER:4300~4349

CHANGE by	XXX	DATE	21-OCT-2002	-A3	CS	1310XXXXX-0-0	XU
DCB PIN	XXXXXXXXXXXX	DCB VER	XXX	SHEET		34 of 100	

D



C

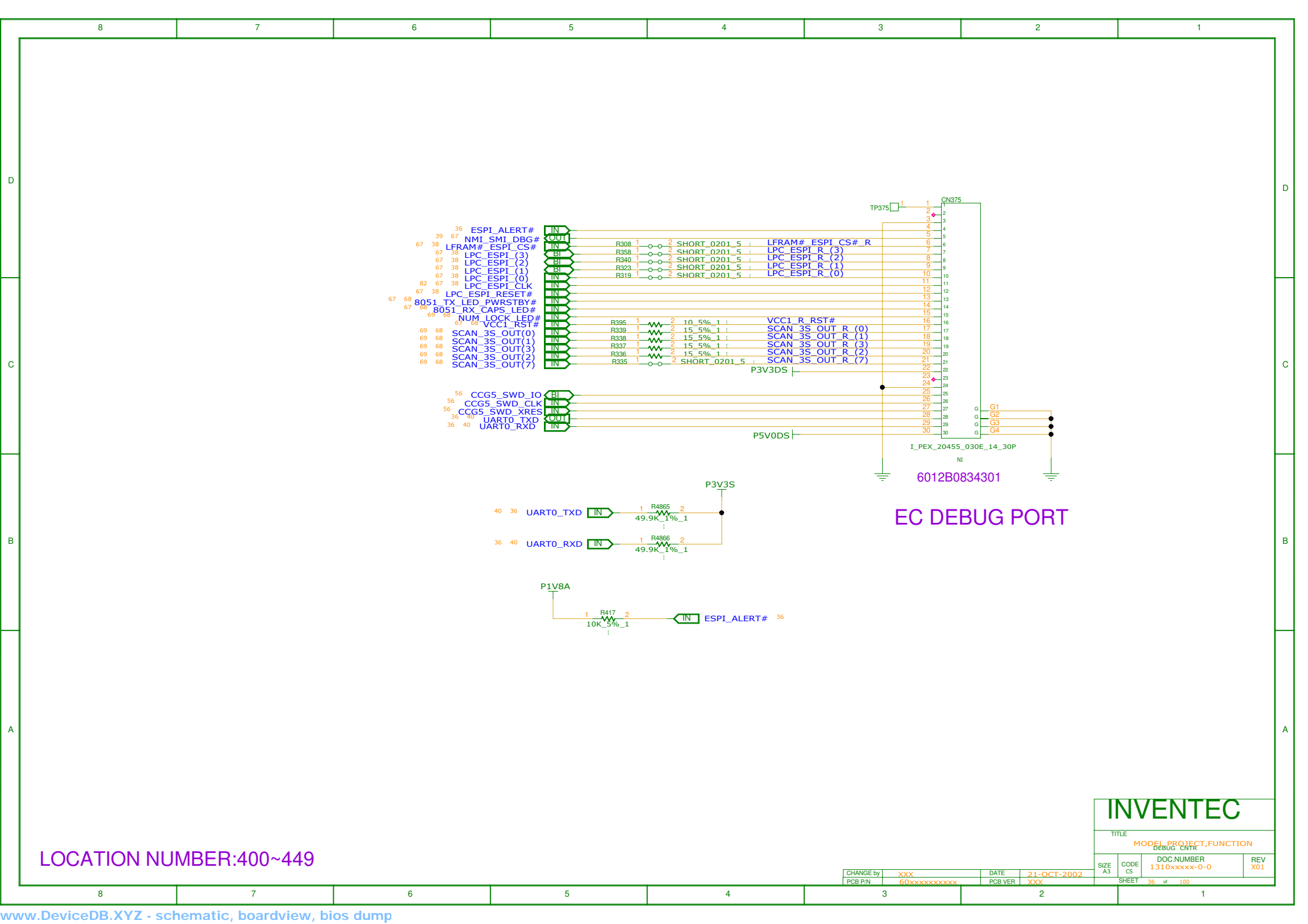


B

B

▲

TITLE			
MODEL,PROJECT,FUNCTION			
XDP & ME COMM			
DOC NUMBER		REV	
1310xxxxx-0-0		X01	
SIZE A3	CODE CS	SHEET	
		35 of 100	



LOCATION NUMBER:400~449

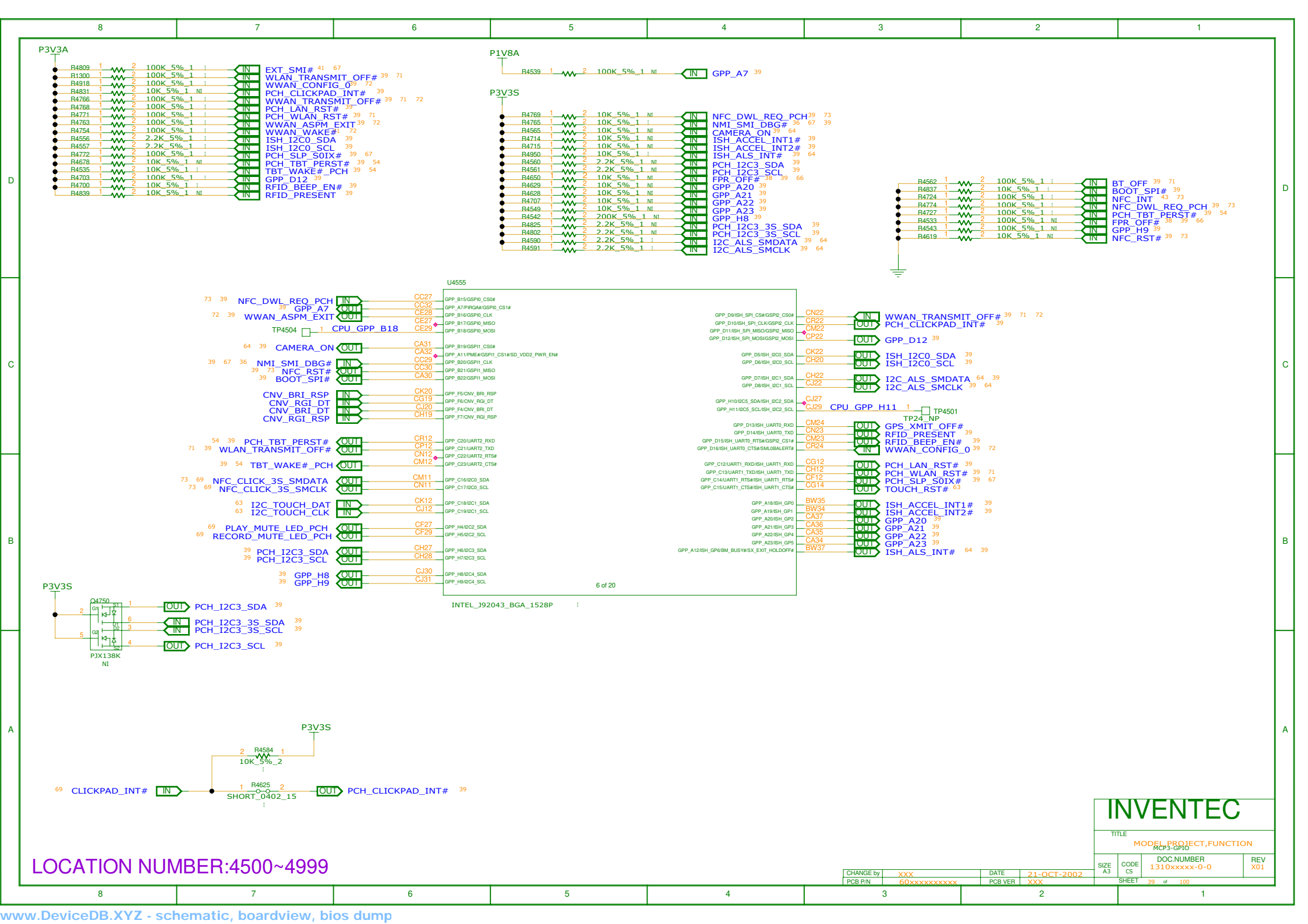
INVENTEC

TITLE
MODEL PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

SHEET 36 of 100

CHANGE by XXX DATE 21-OCT-2002
PCB P/N 60xxxxxxxxxxx PCB VER XXX

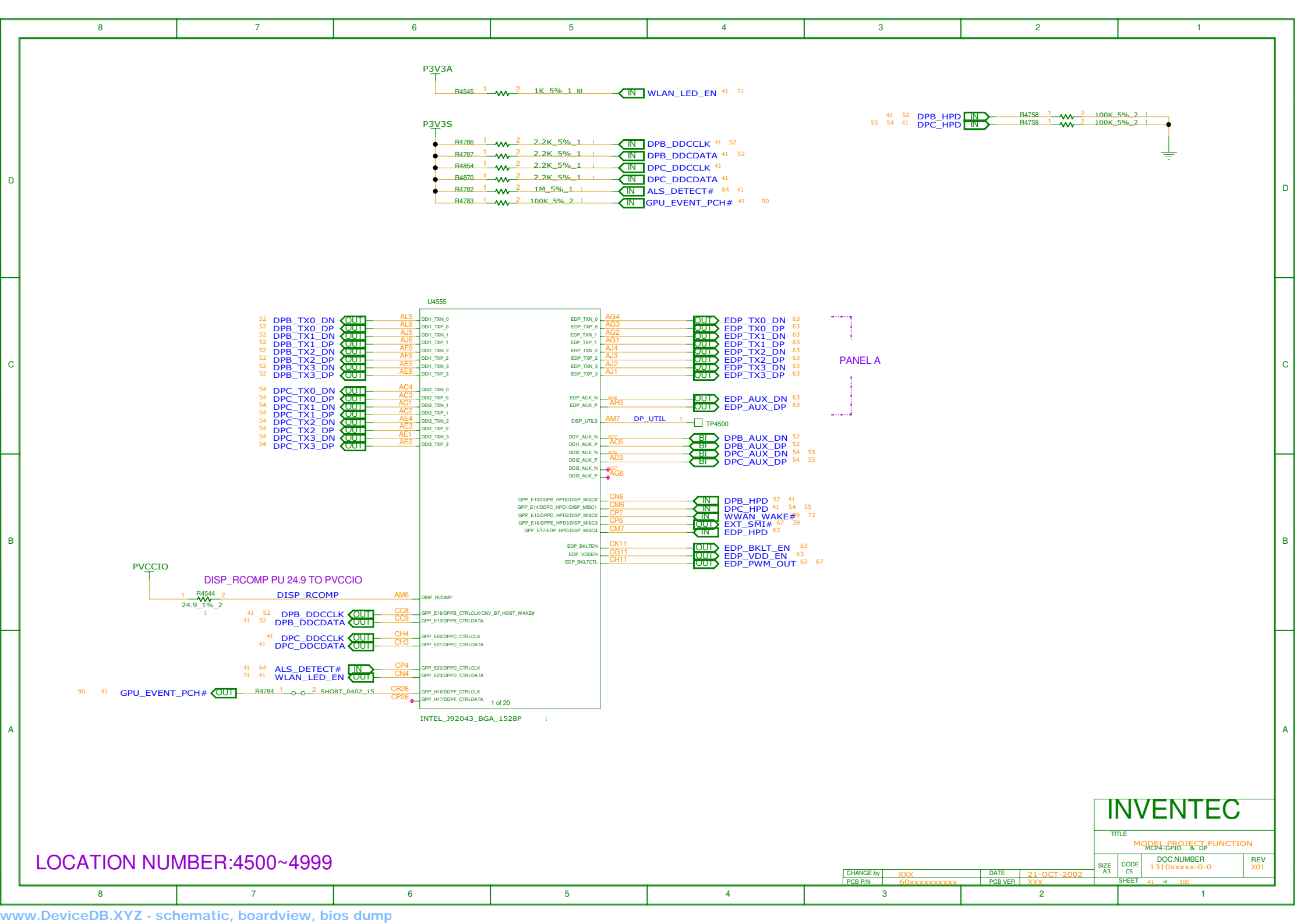


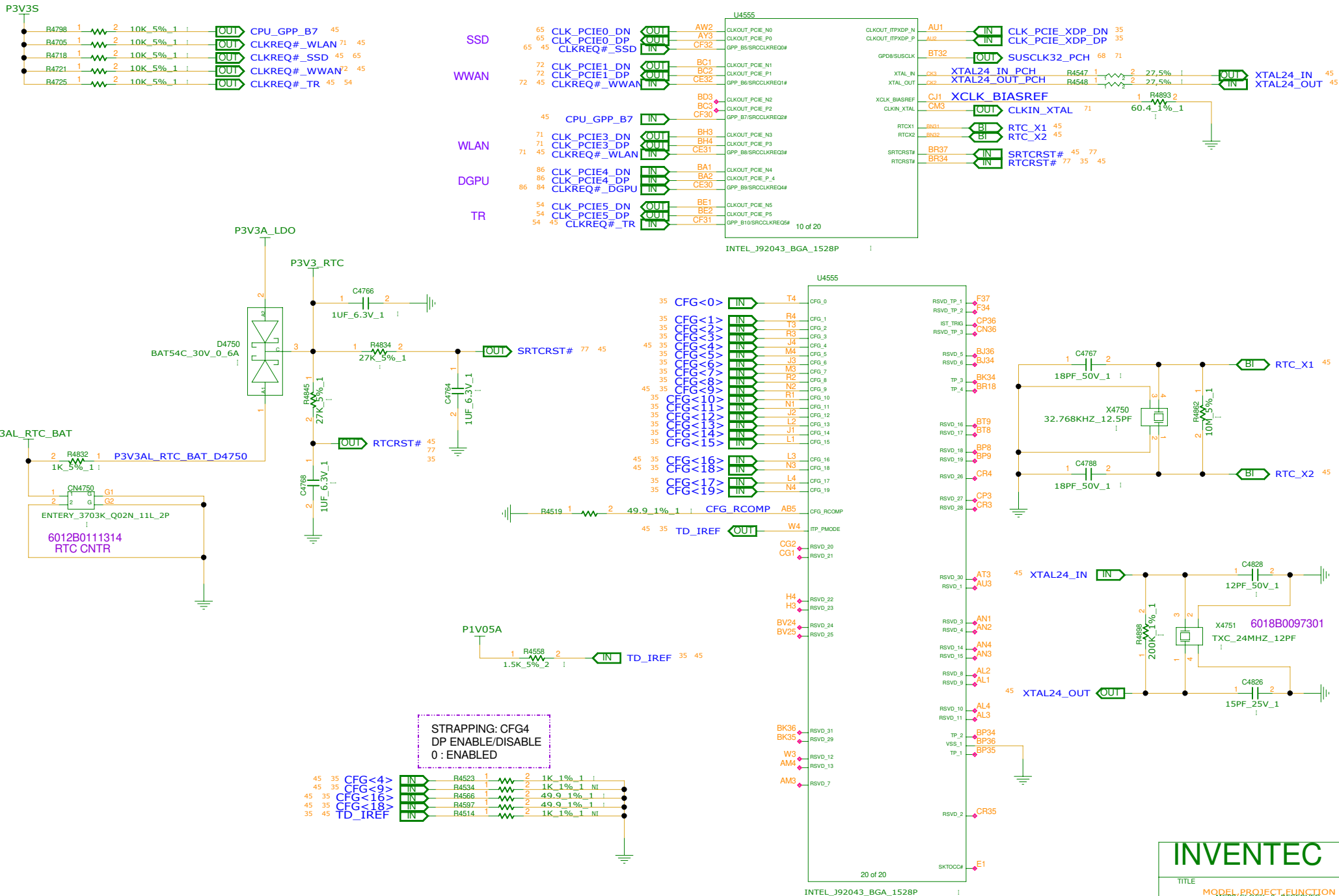


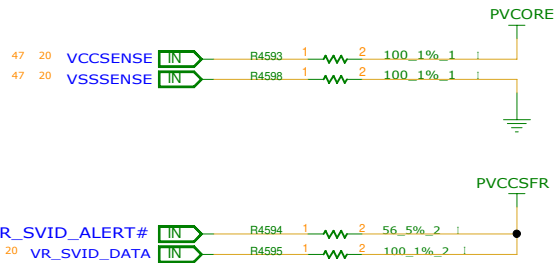
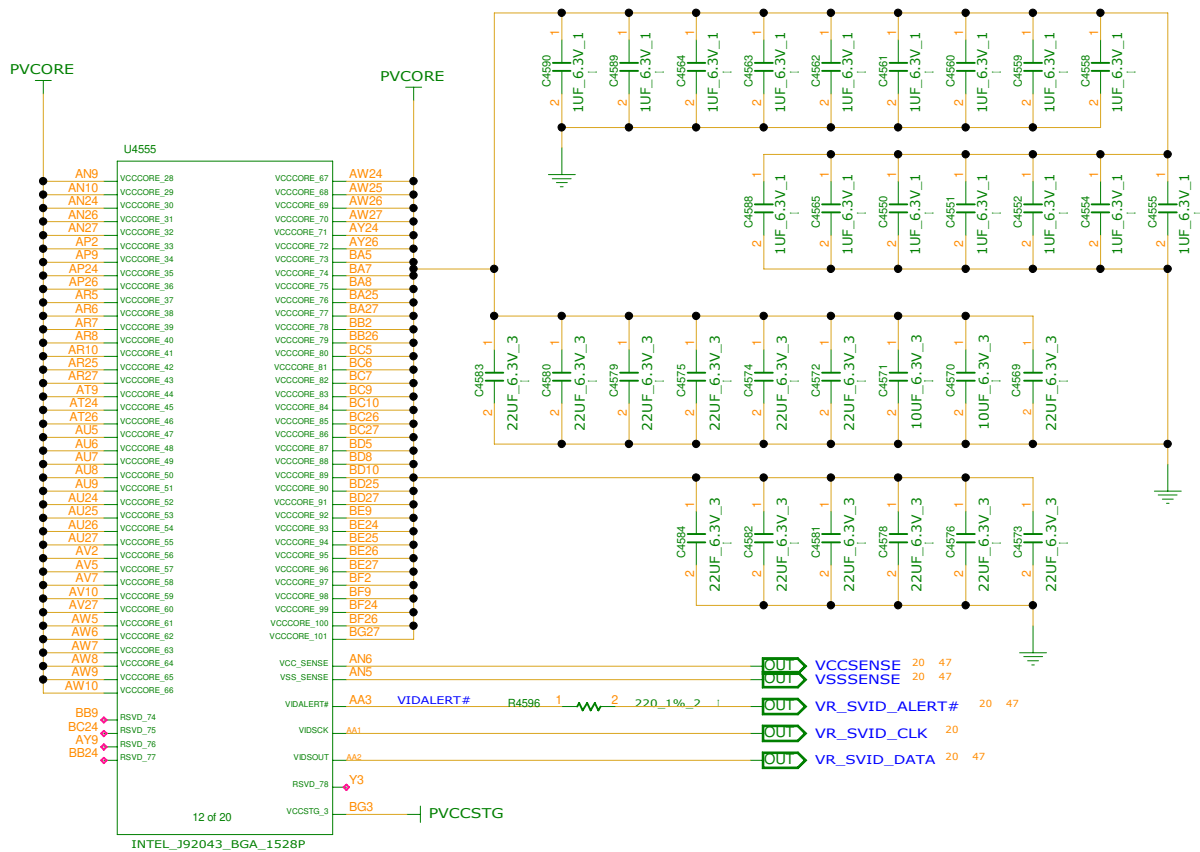
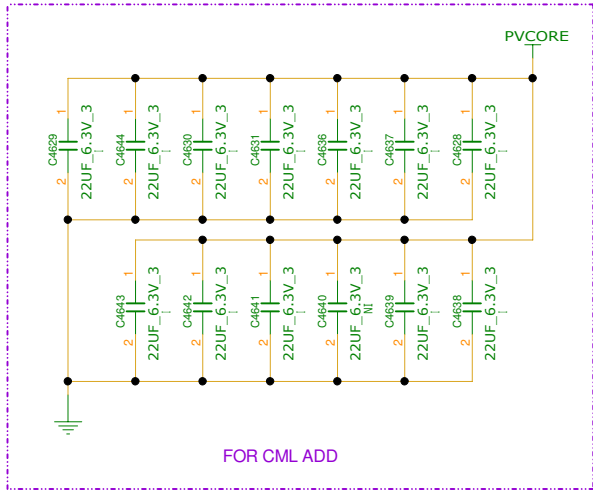
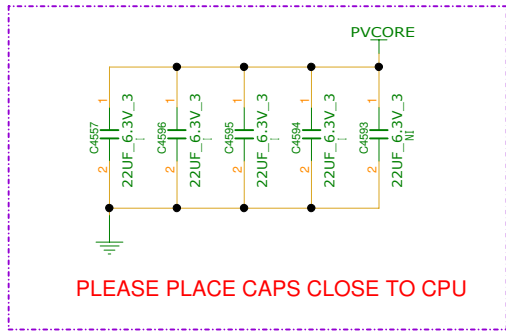
	PLT_ID1
VPRO	0
NON VPRO	1

SIZE A3	CODE CS	1310xxxxx-0-0	X01
SHEET		40 of 100	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX



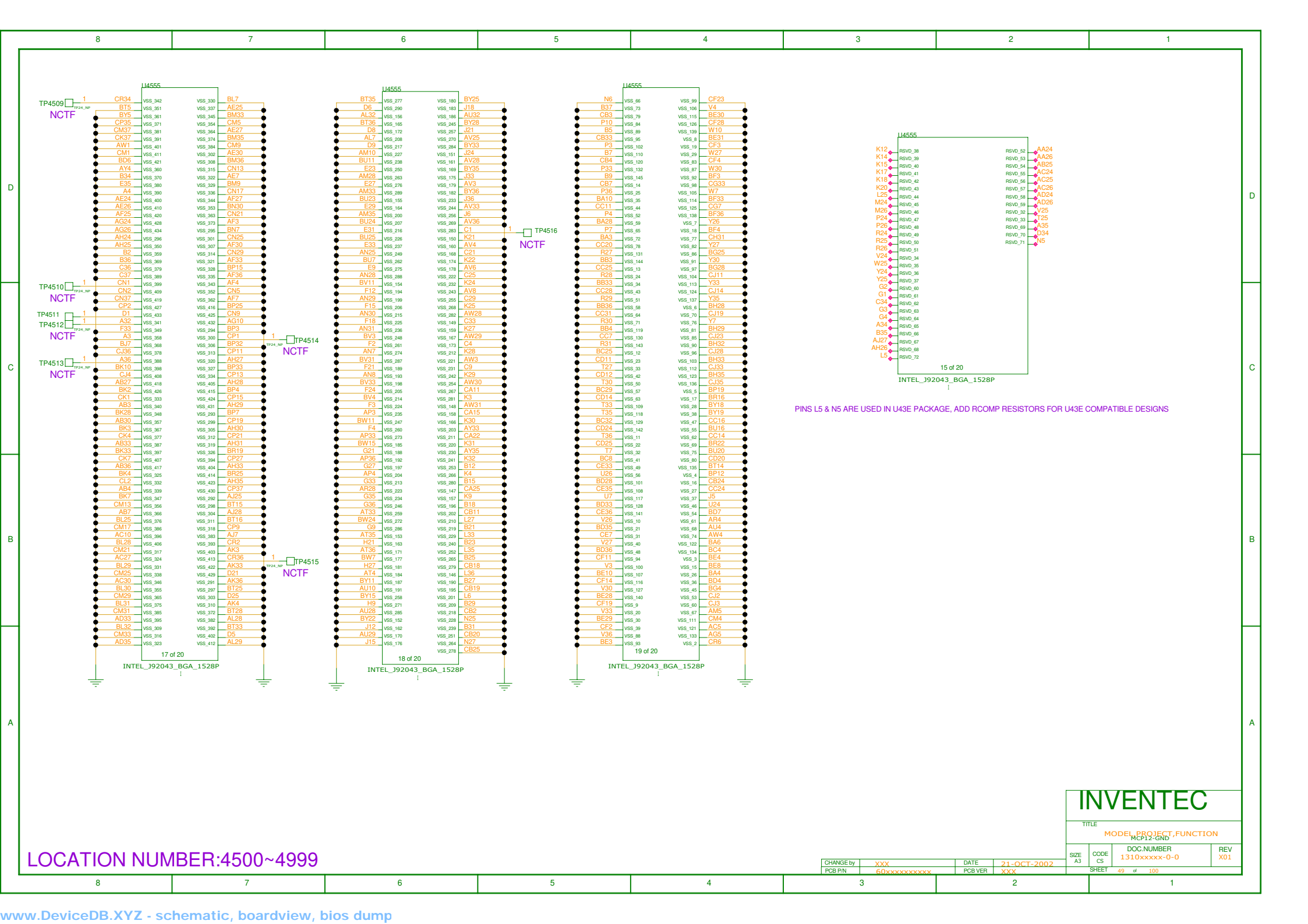




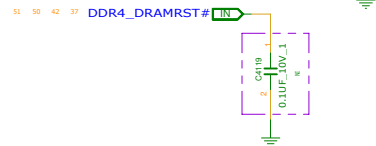
LOCATION NUMBER:4500~4999

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

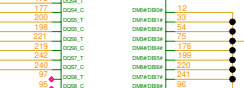
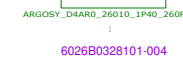
INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 47 of 100			



Δ



PLACE C4119 WITHIN 200 MILS
FROM THE SODIMM - 0 (CN4100)



SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	REV X01
SHEET		50	of 100

www.DeviceDB.XYZ - schematic, boardview, bios dump

F

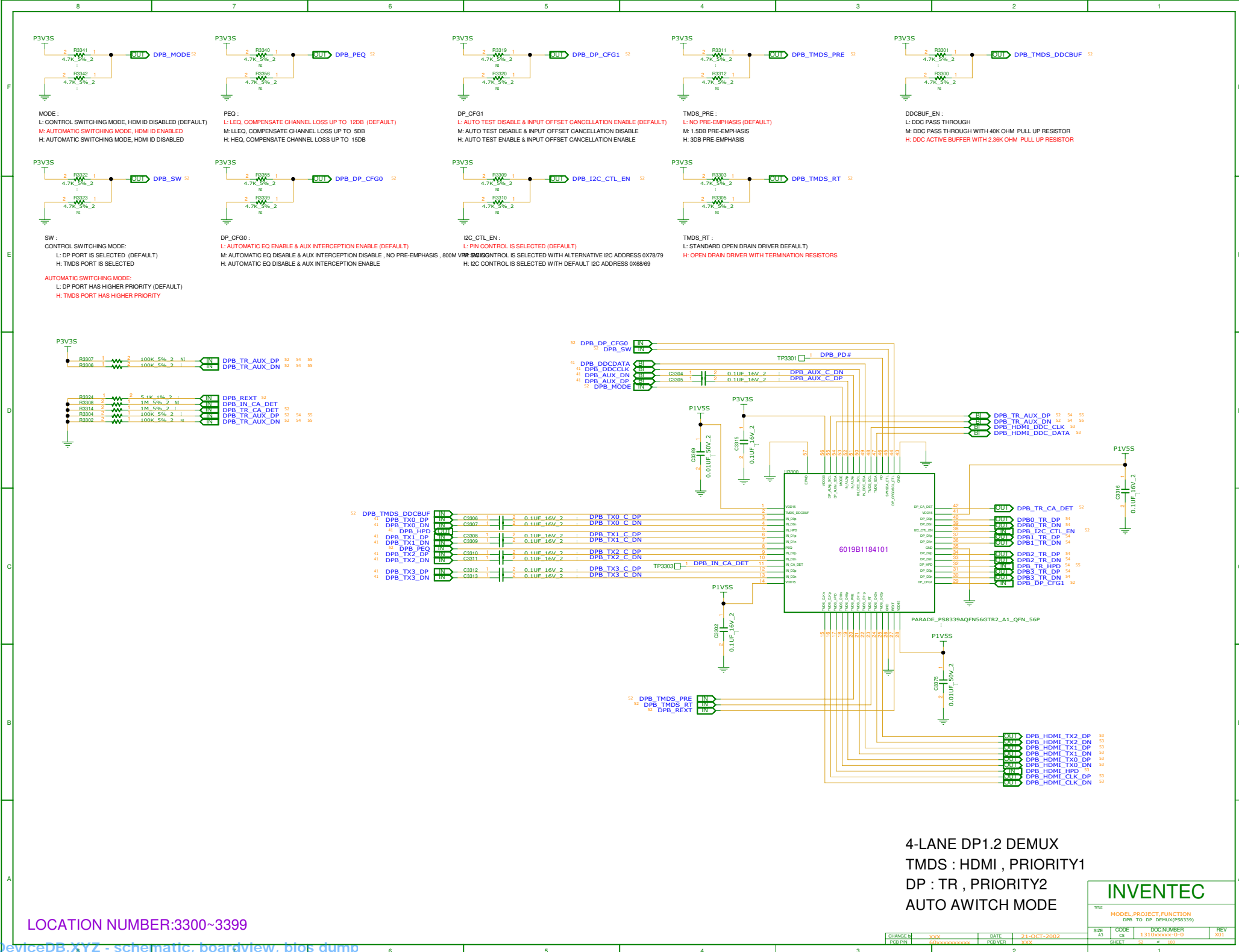


C

B

A

FE10C8A



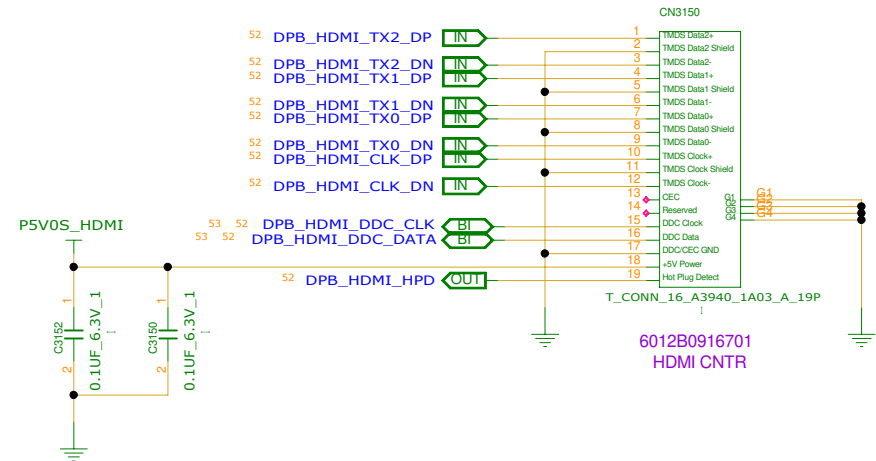
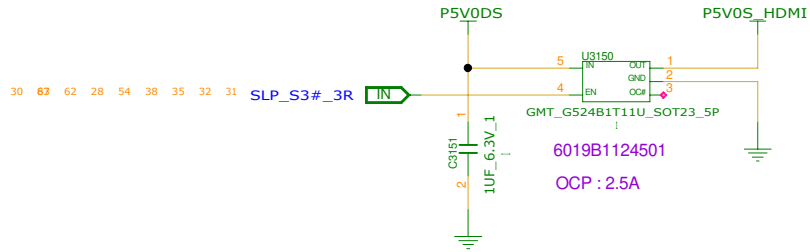
LOCATION NUMBER:3300~3399

4-LANE DP1.2 DEMUX
TMD5 : HDMI , PRIORITY1
DP : TR , PRIORITY2
AUTO AWITCH MODE

INVENTEC			
MODEL/PROJECT/FUNCTION			
DPB TO DP DEMUX(P3339)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310XXXXXX-0-0	201
SHEET	52	#	108

CHANGEN	XXX	DATE	21-OCT-2002
PCB PIN	6019B1184101	PCB VER	XXX

HDMI



LOCATION NUMBER:3150~3199

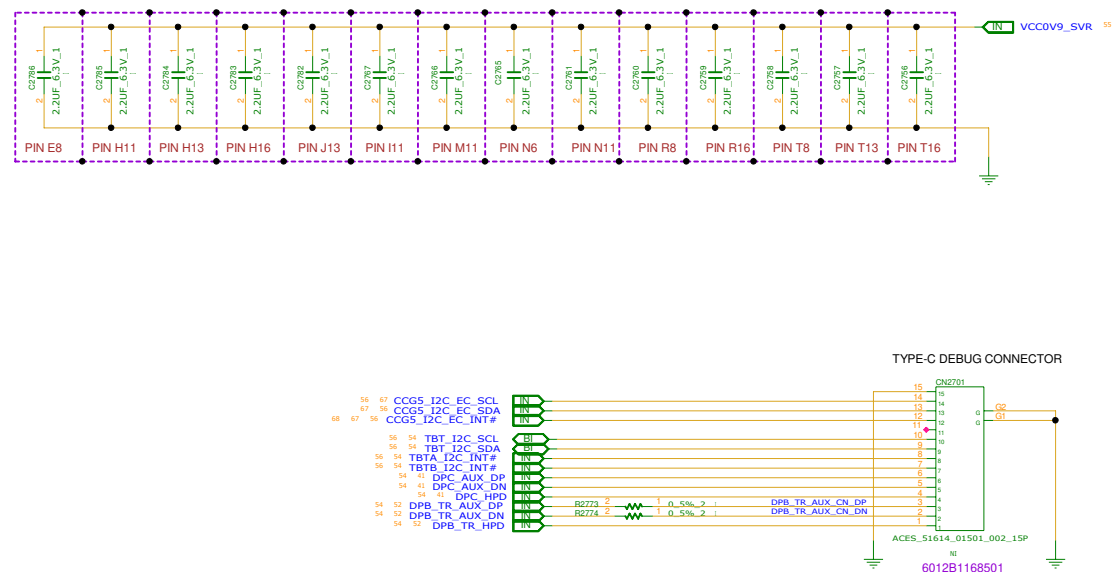
INVENTEC

TITLE MODEL,PROJECT,FUNCTION
HDMI

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		53 of 100	

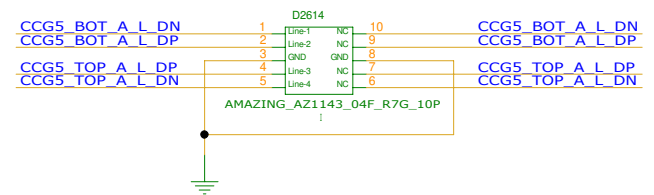
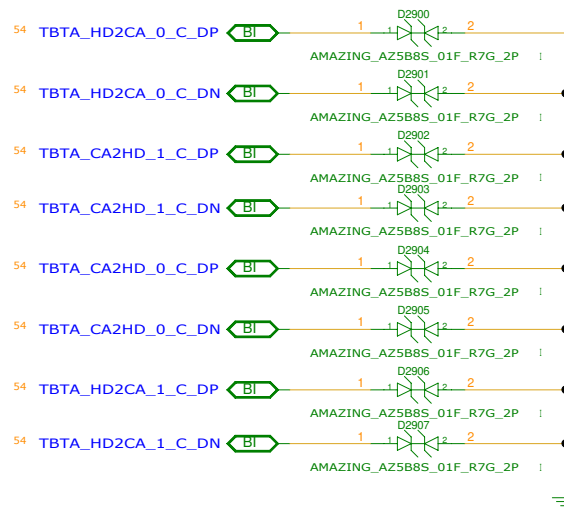
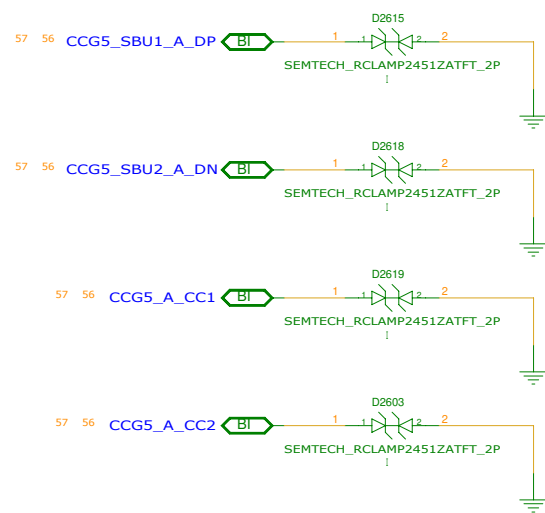
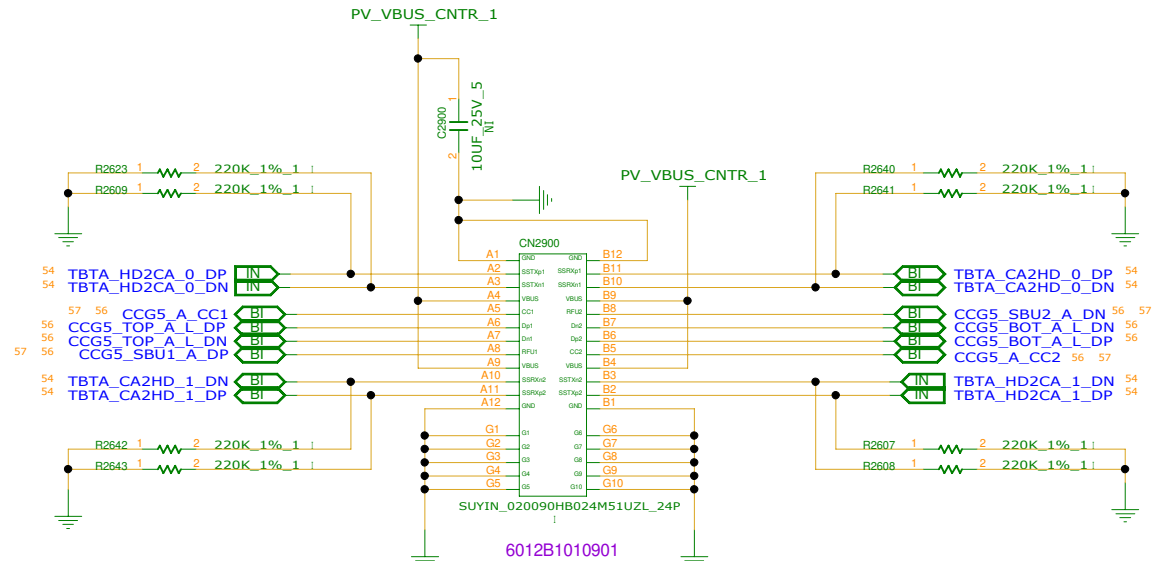
CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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LOCATION NUMBER:2700~2949





USB TYPE-C PORT A USB TYPE - C CONN



LOCATION NUMBER:2600~2699

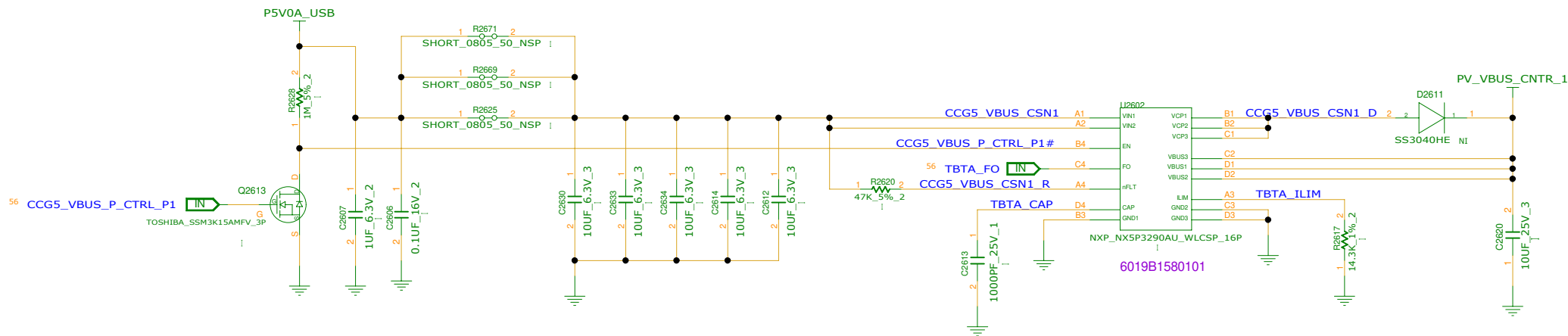
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION USB TYPE-C A-1 (RIGHT)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		of 57	100

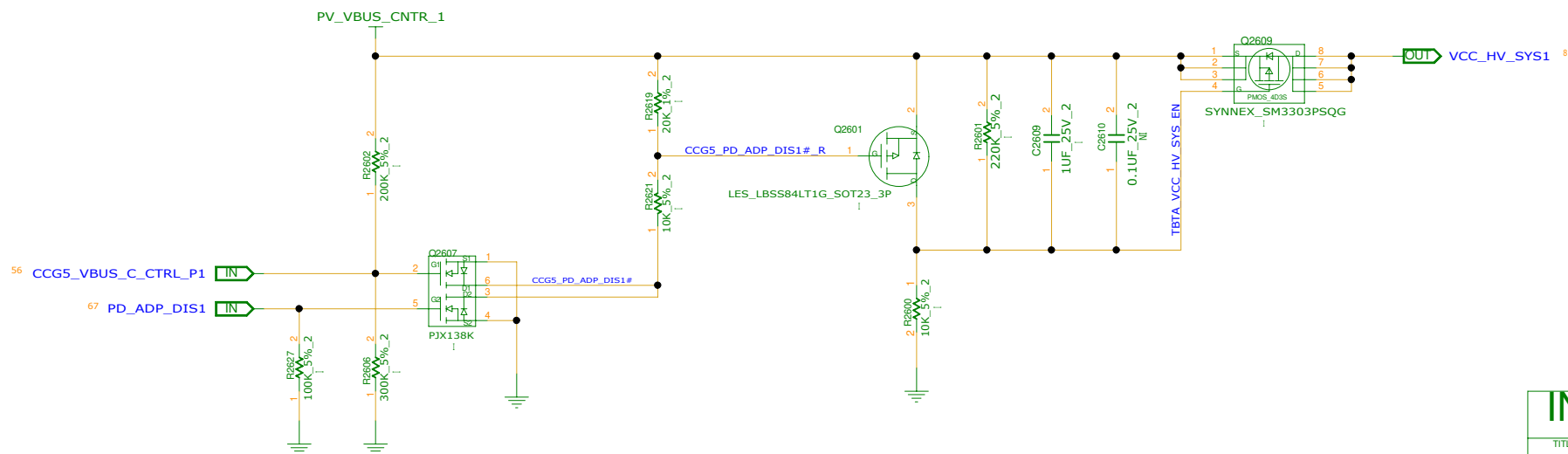
CHANGE by	XXX	DATE	
PCB P/N	60N6xxxxxxx	PCB VER	XXX-OCT-2002

USB TYPE-C PORT A CCG5(RIGHT) PD CONTROLLER HIGH / LOW VOLTAGE PATH

LOW VOLTAGE PATH



HIGH VOLTAGE PATH



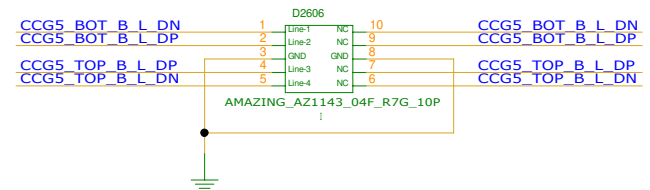
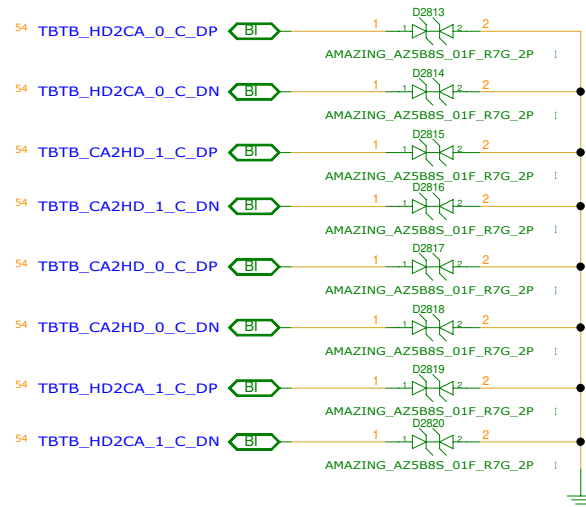
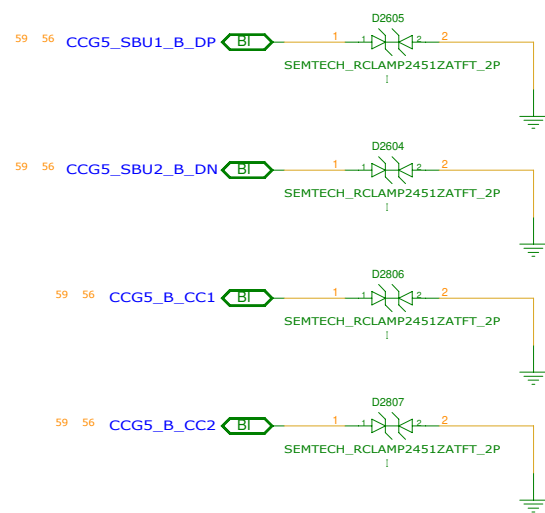
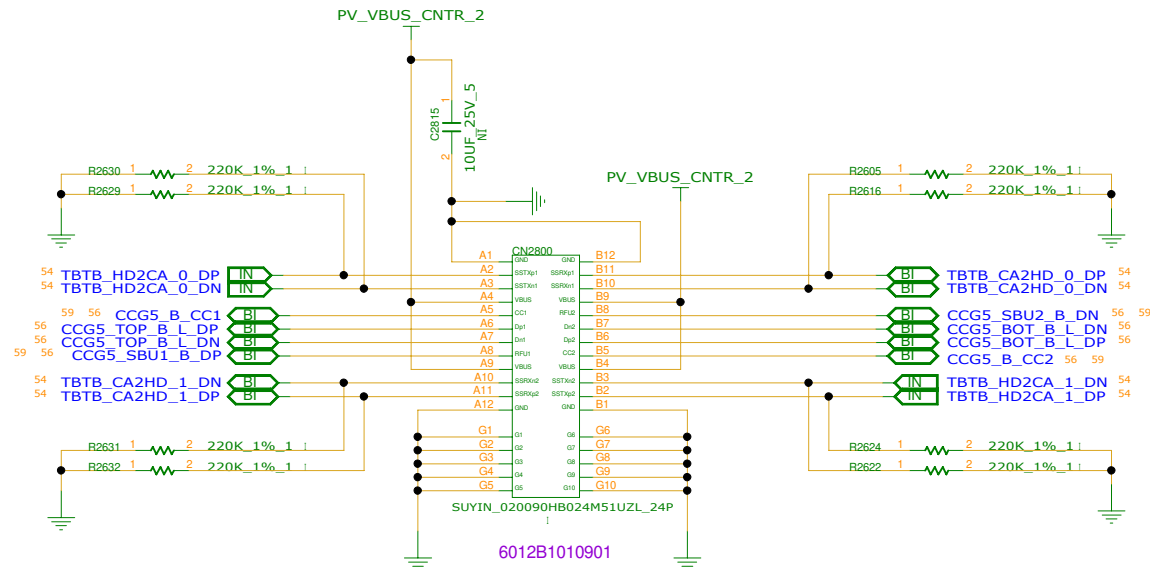
LOCATION NUMBER:2600~2699

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION USB TYPE-C A-2 (RIGHT)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET	58	of	100

CHANGE by PCB P/N	XXX 60xxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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USB TYPE-C PORT B USB TYPE - C CONN



LOCATION NUMBER:2600~2699

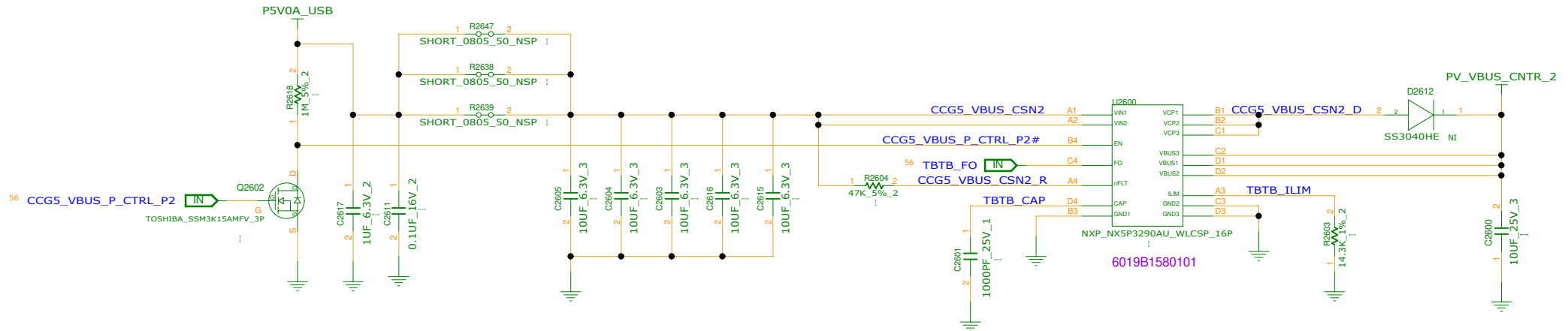
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION USB TYPE-C B-1 (RIGHT)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 59 of 100			

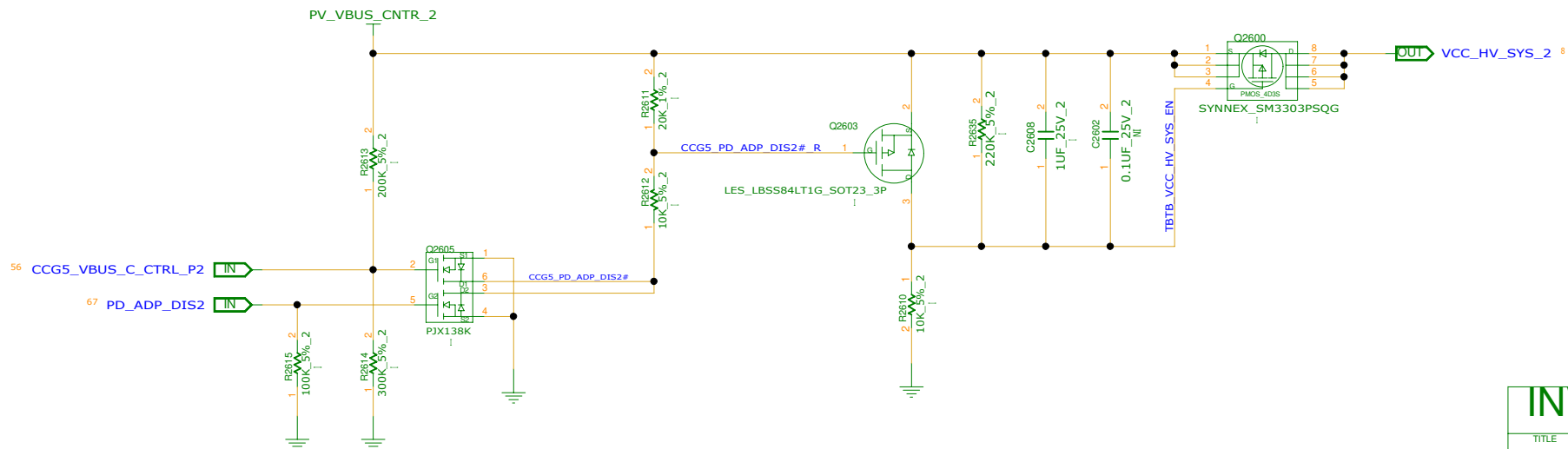
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

USB TYPE-C PORT A CCG5(RIGHT) PD CONTROLLER HIGH / LOW VOLTAGE PATH

LOW VOLTAGE PATH



HIGH VOLTAGE PATH



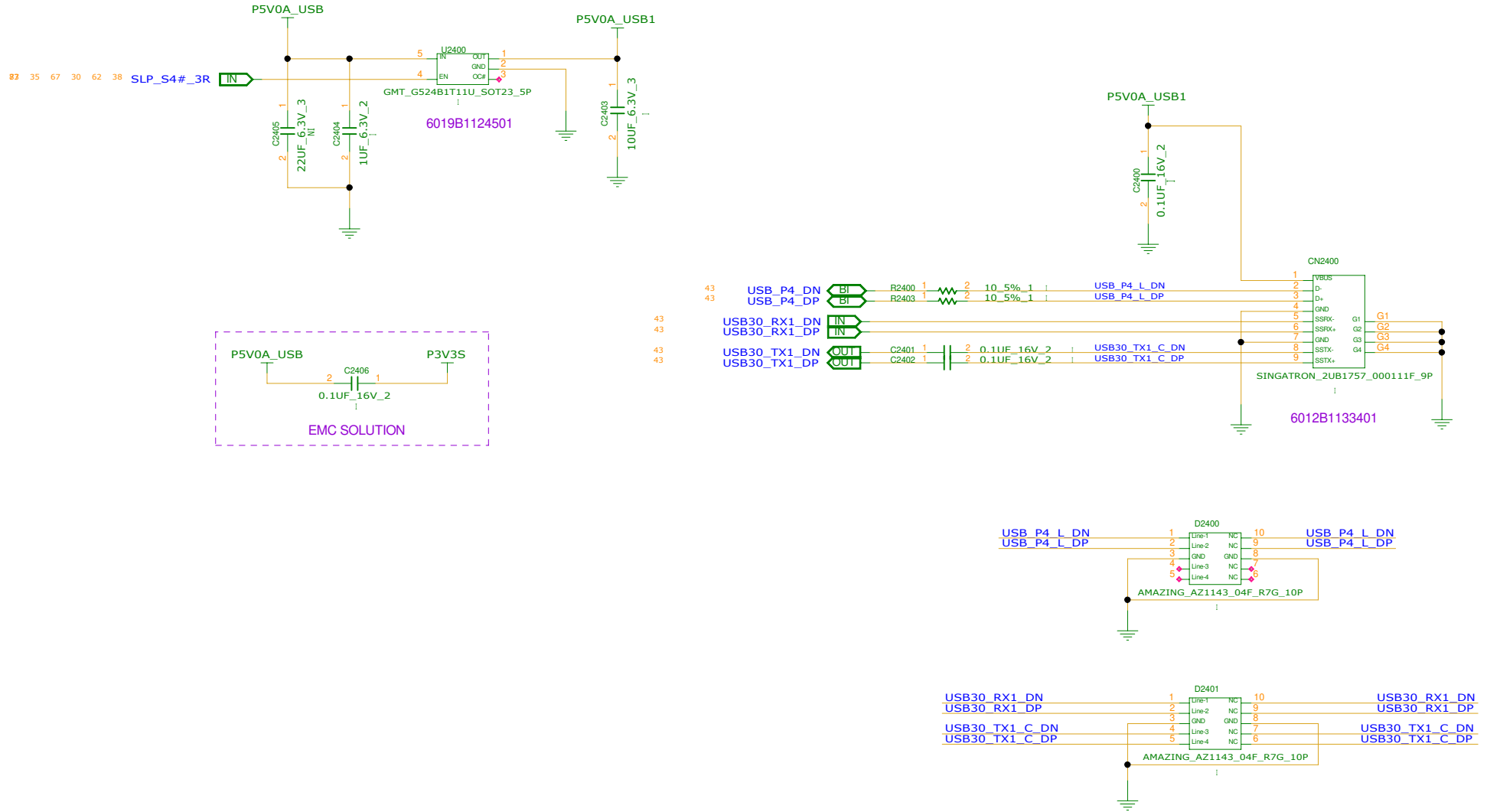
LOCATION NUMBER:2600~2699

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION USB TYPE-C B-2 (RIGHT)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		60 of 100	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

USB TYPEA PORT_B



INVENTEC

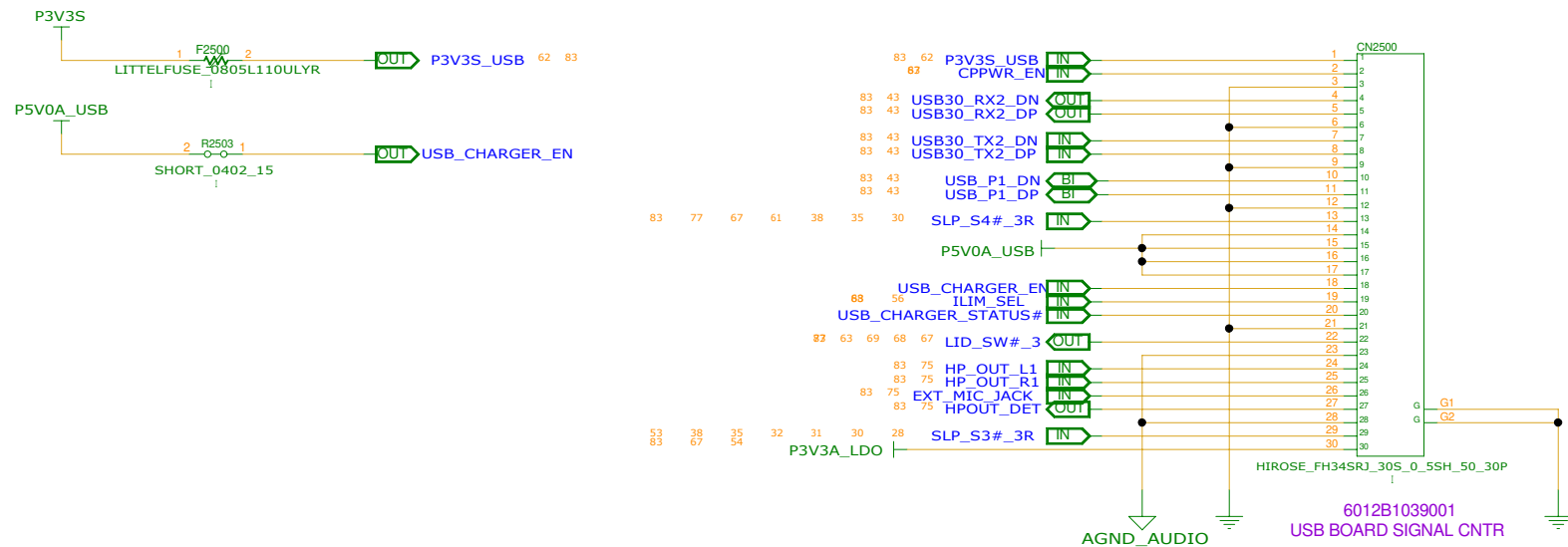
TITLE
MODEL PROJECT FUNCTION
USB_TYPEA-1_RIGHT

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N 60xxxxxxxxxx PCB VER XXX

SHEET 61 of 100

M/B TO USB/B CNTR



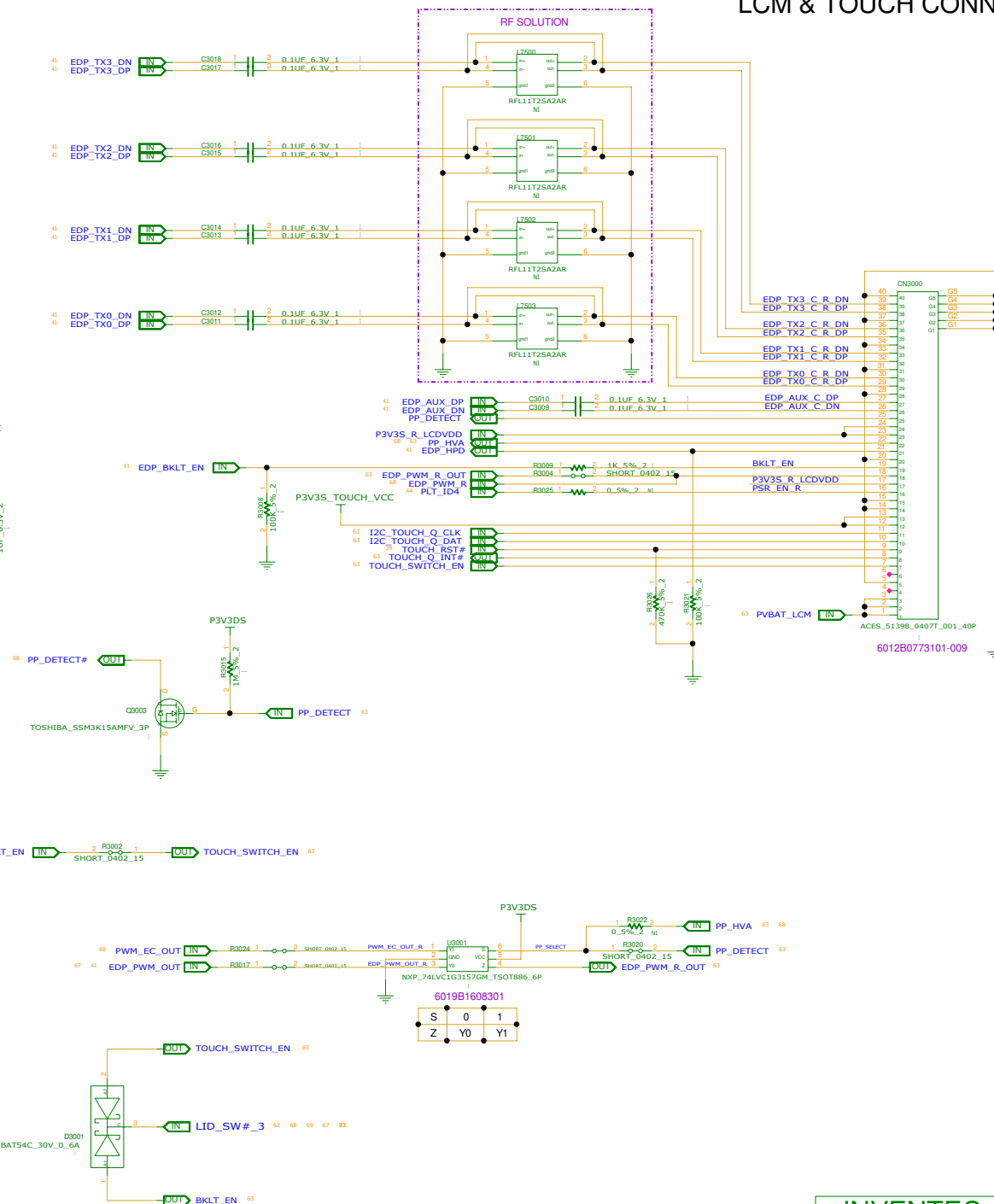
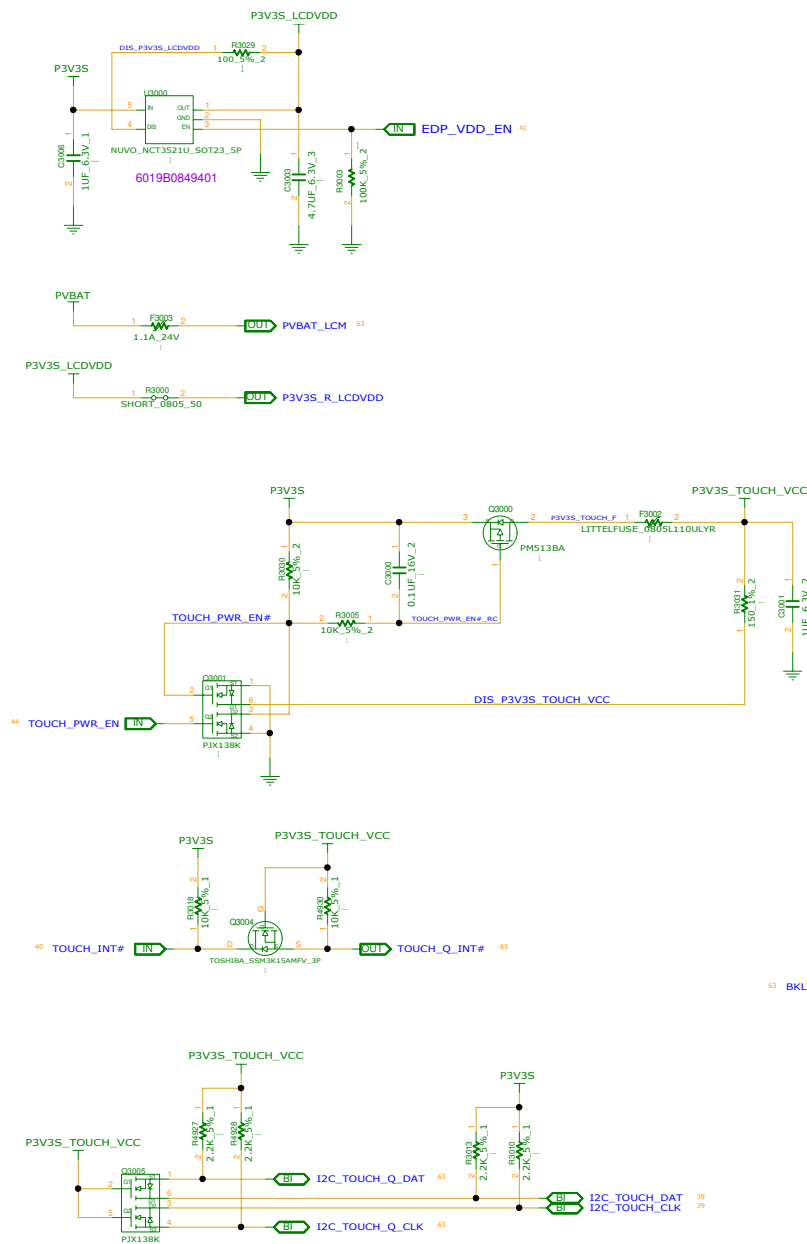
LOCATION NUMBER:2500~2599

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
USB_DB_CONN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		62 of 100	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

LCM & TOUCH CONN

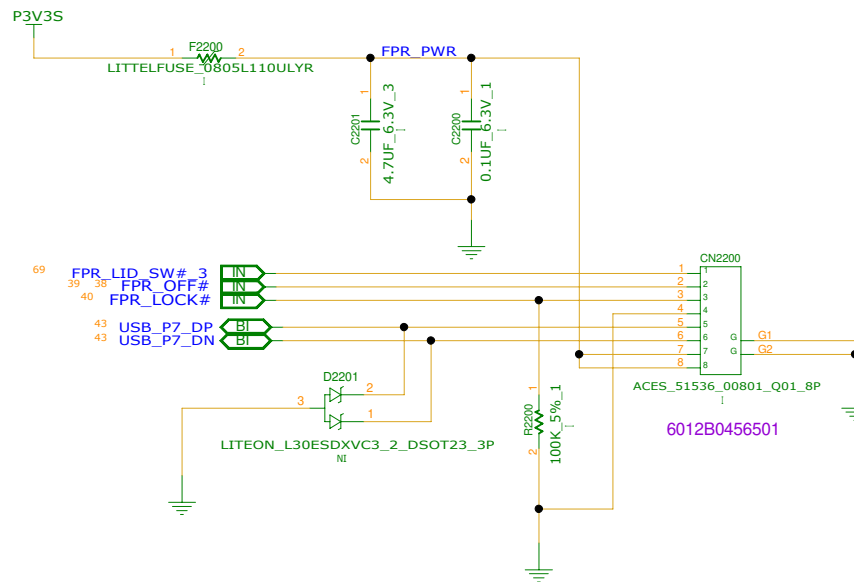


LOCATION NUMBER:3000~3049

A



CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	1310xxxxx-0-0	X01
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX	SHEET	65	of	100		



FINGER PRINT CONN

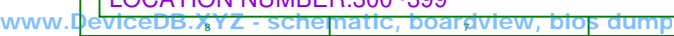
LOCATION NUMBER:2200~2249

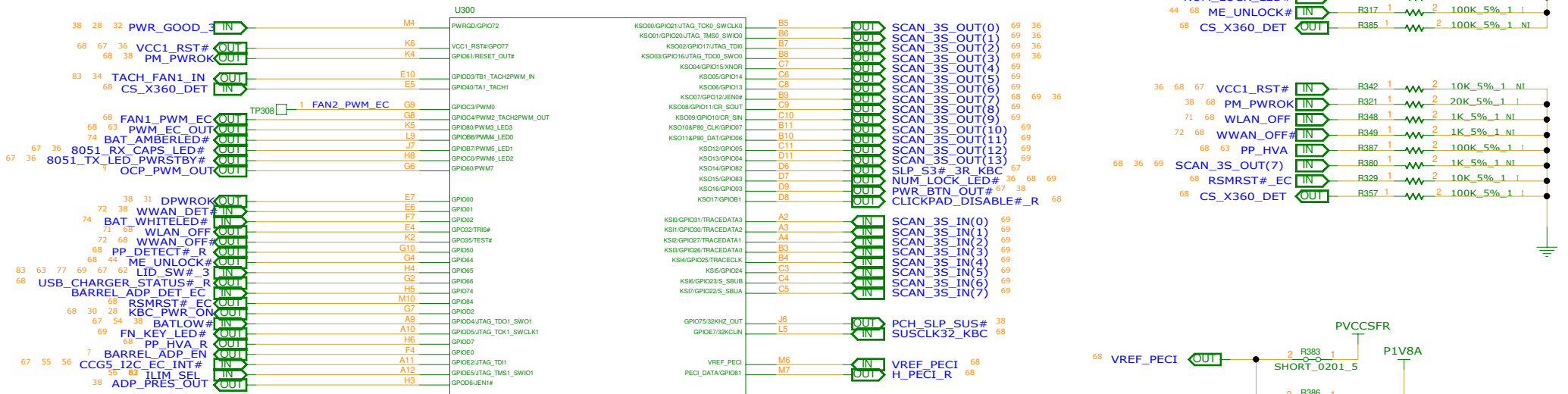
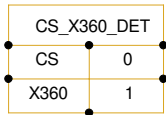
INVENTEC

TITLE
MODEL PROJECT,FUNCTION
FINGER PRINTER

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 66 of 100			

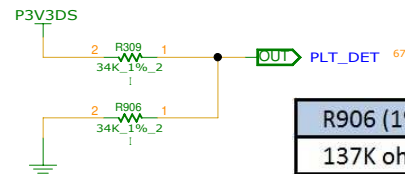
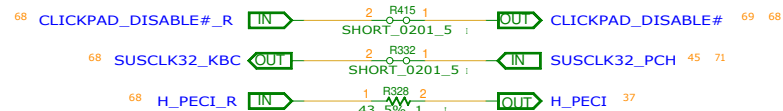
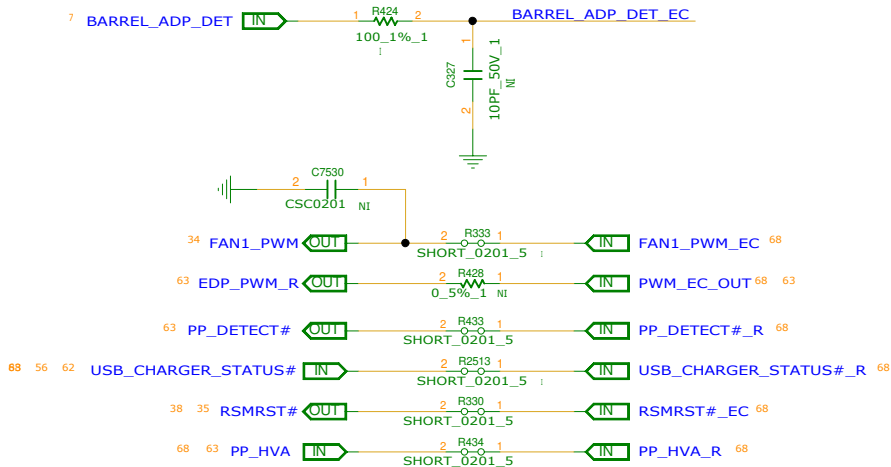
CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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NUVOTON_NPCX797H1A1BX_VFBGA_144P

6019B1811501



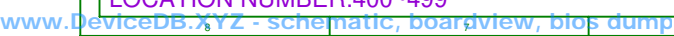
R906 (1%)	"PLT_DET" CONFIGURATION SETTING
137K ohm	CS U22 UMA SKU
80.6K ohm	CS U22 DIS SKU
51.1K ohm	CS U42/U62 UMA SKU
34K ohm	CS U42/U62 DIS SKU

LOCATION NUMBER:300~399

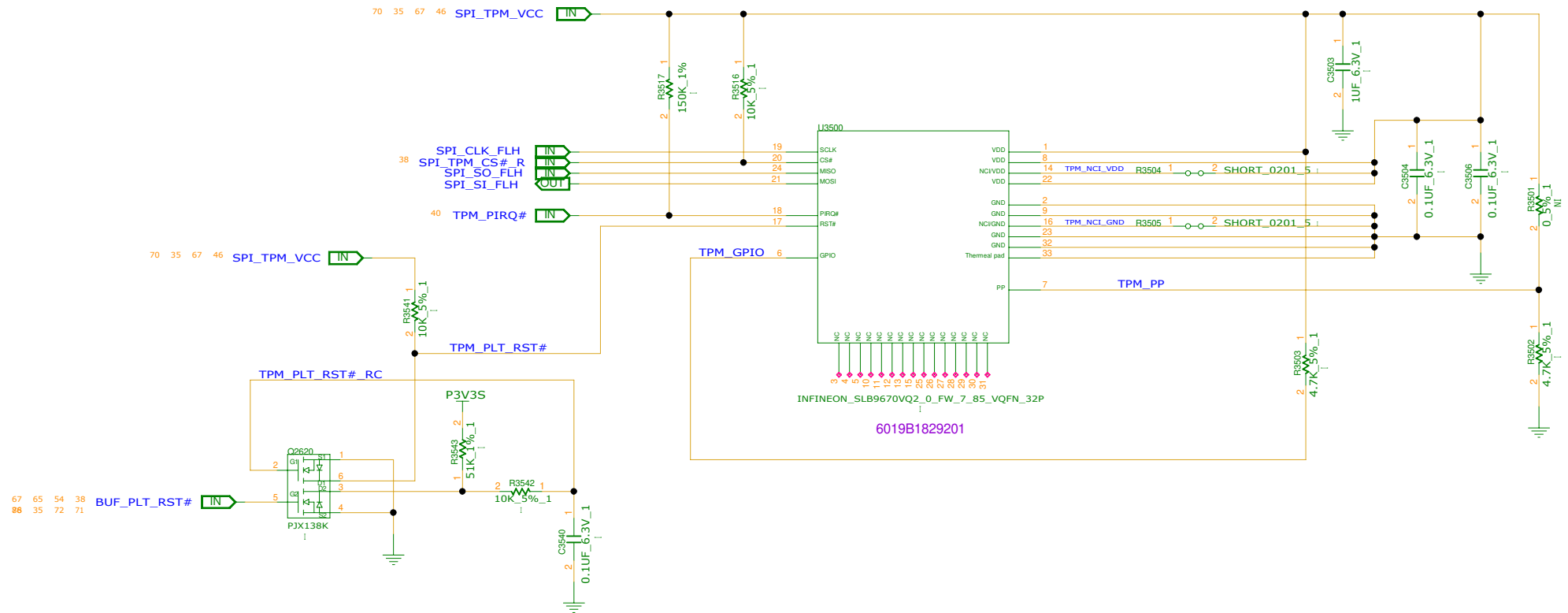
INVENTEC

TITLE			
MODEL PROJECT, FUNCTION			
KBC 2			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET	68	of 100	1133

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX



TPM2.0



LOCATION NUMBER:3500~3599

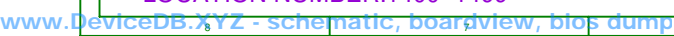
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX

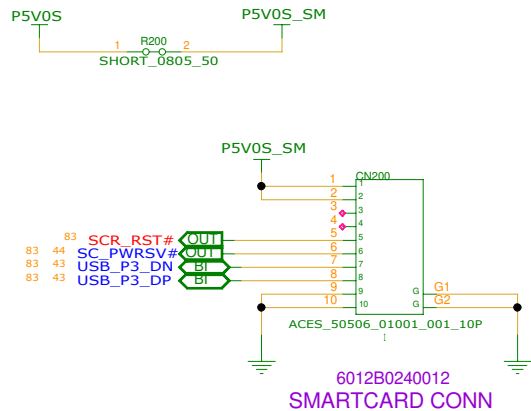
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

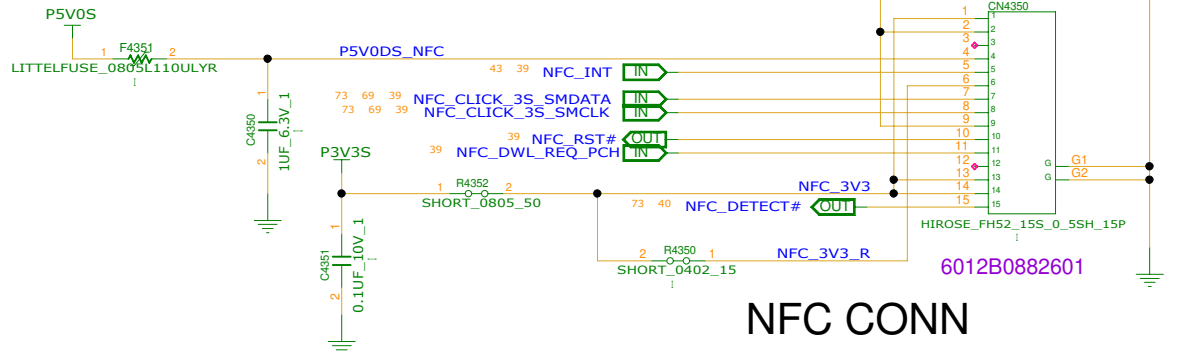
SHEET 70 of 100





NOTE : CONNECT TO CN9000 (P.79)
SMART CARD D/B W TO B CONN

NFC	PIN
3.3V	1
GND	2
USB D+	3
5V	4
NFC_INT	5
3.3V	6
I2C_DATA	7
I2C_CLK	8
GND	9
NFC_RST#	10
NFC_DWL_REQ	11
USB D-	12
3.3V	13
3.3V	14
NFC_DETECT#	15



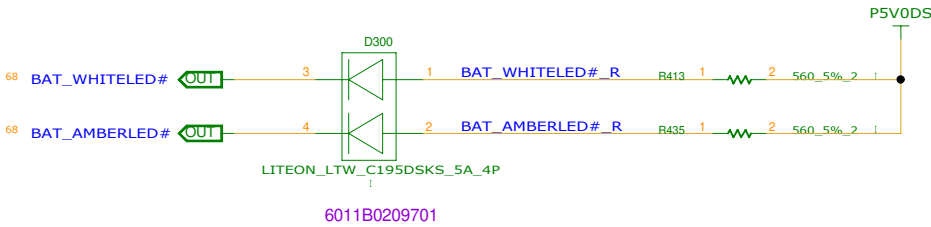
LOCATION NUMBER:850~999

LOCATION NUMBER:4350~4399

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

INVENTEC			
TITLE MODEL PROJECT FUNCTION SMART CARD & NFC RTD			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 73 of 100			

POWER JACK LED



LOCATION NUMBER:100~199

INVENTEC

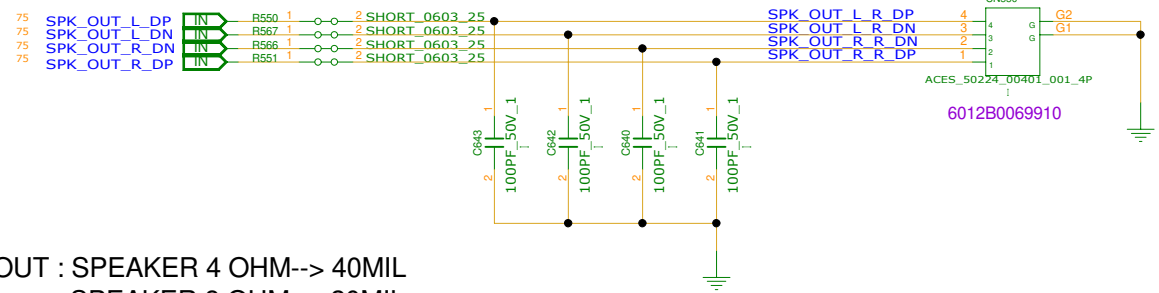
TITLE MODEL PROJECT FUNCTION POWER JACK LED

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002 PCB P/N 60xxxxxxxxxxx PCB VER XXX

SHEET 74 of 100





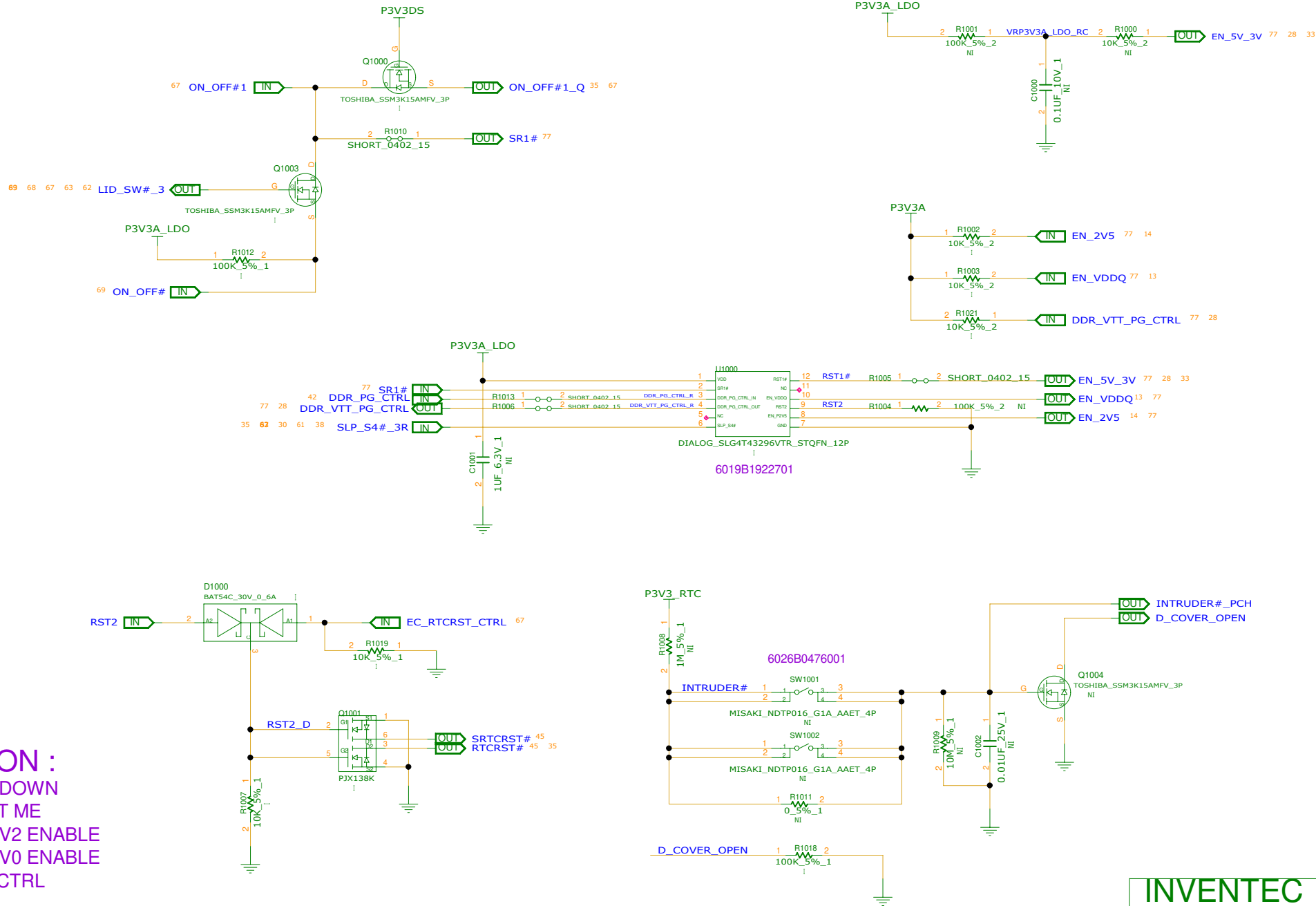
LAYOUT : SPEAKER 4 OHM--> 40MIL
SPEAKER 8 OHM--> 20MIL

INTERNAL SPEAKER

REFERENCE NUMBER:600~649

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
SPEAKER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		76 of 100	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



FUNCTION :

1. 12S SHUTDOWN
2. 24S RESET ME
3. P2V5 & P1V2 ENABLE
4. P3V3 & P5V0 ENABLE
5. DDR_PG_CTRL

LOCATION NUMBER : 1000~1099

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
12S SHUT DOWN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 77 of 100			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

D

C

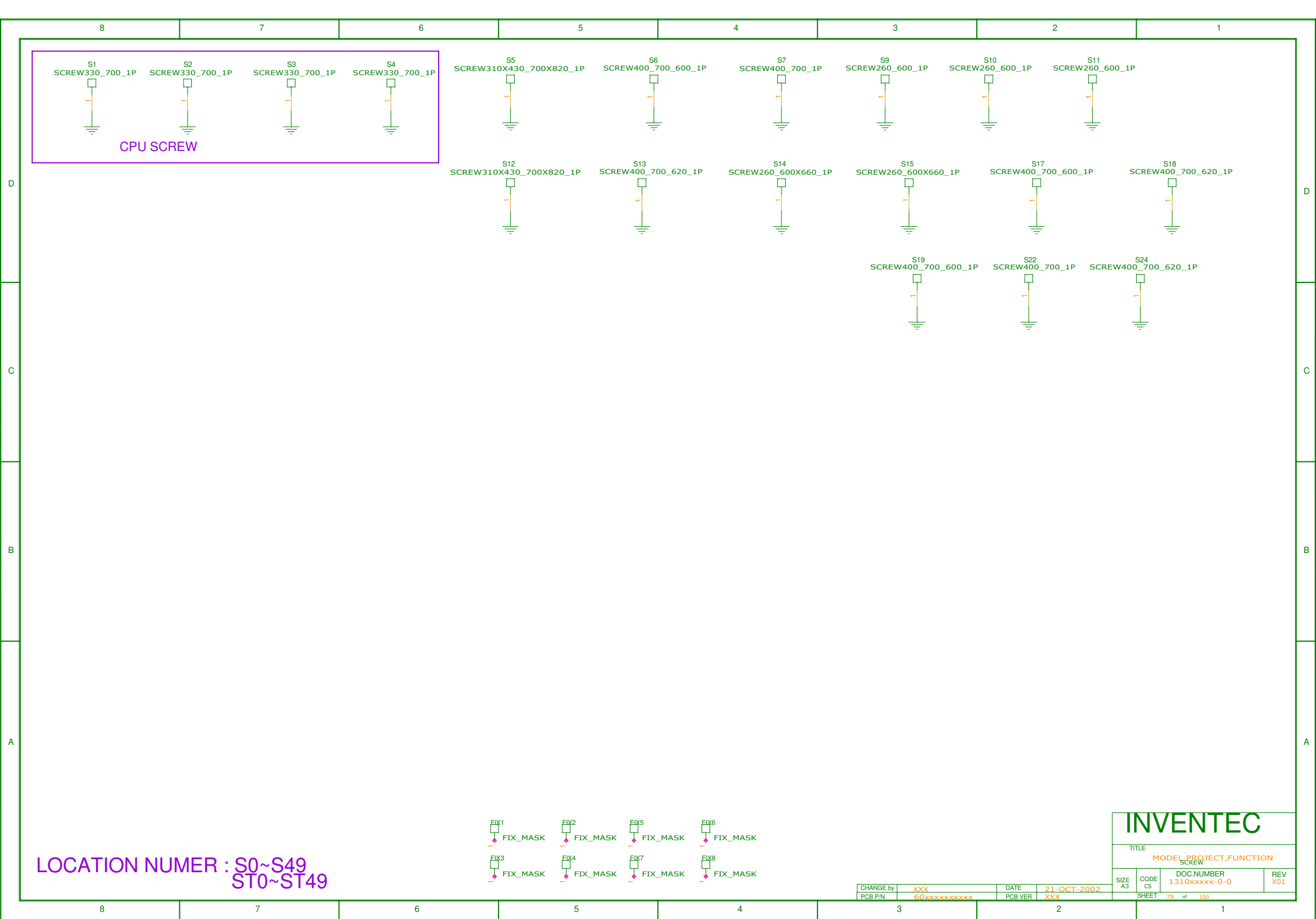
LOCATION NUMBER : 3800~3849

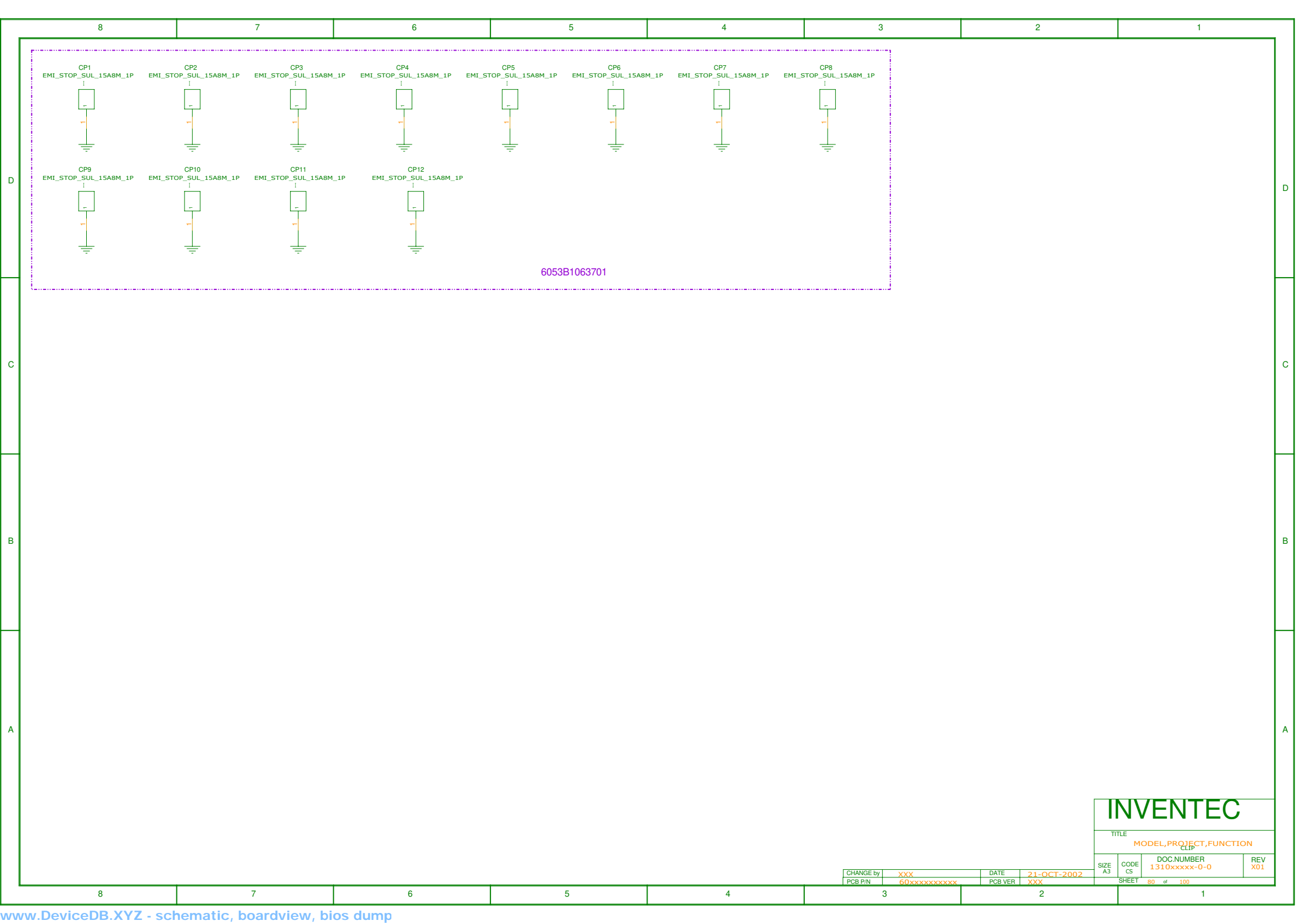
CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	1310xxxxx-0-0	X01
PCB P/N	60xxxxxxxxxx	PCB VER	XXX	SHEET	78	of	100		

INVENTEC

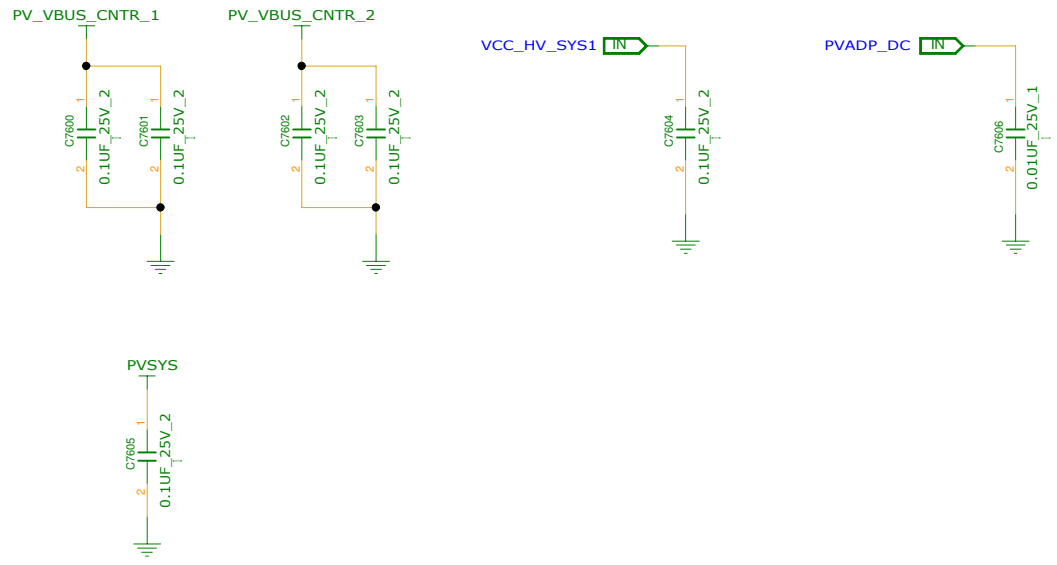
TITLE	MODEL,PROJECT,FUNCTION
EMMC	

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET		78 of 100	





EMI SOLUTION

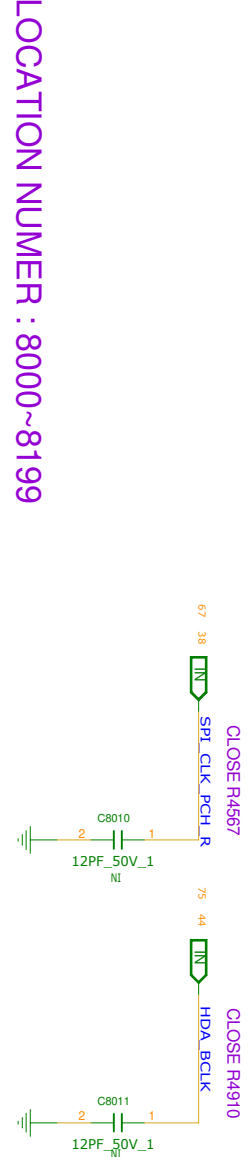
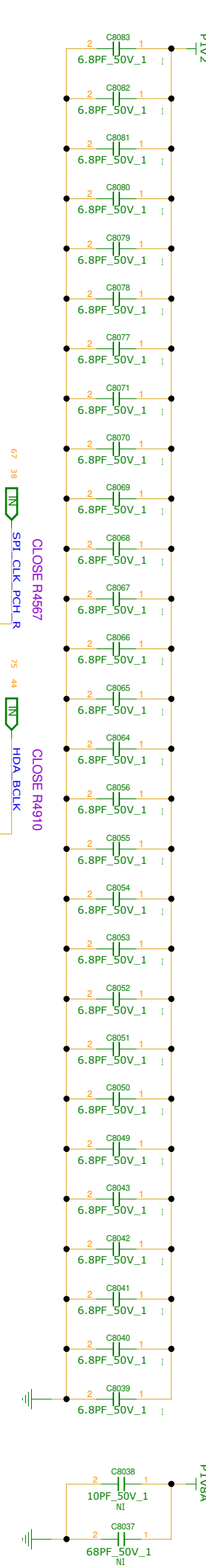
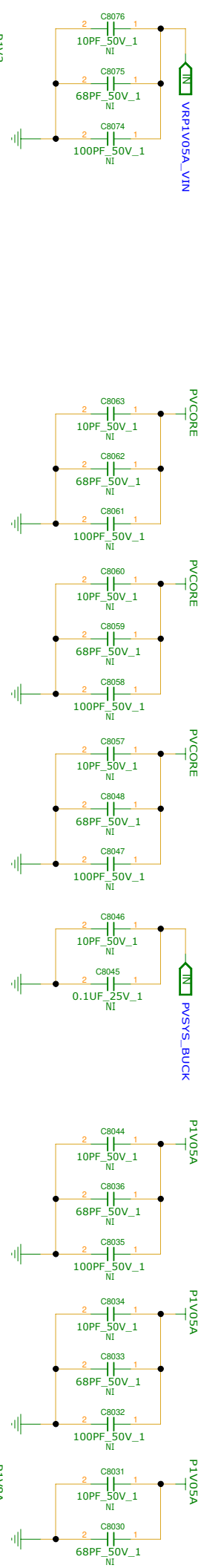
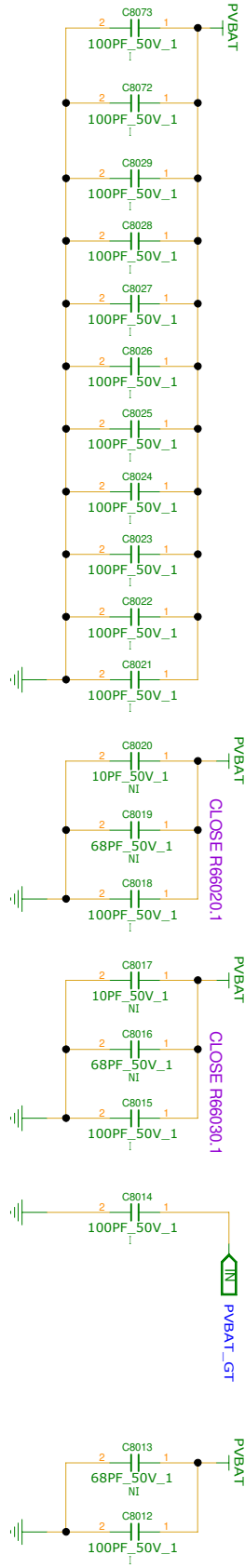
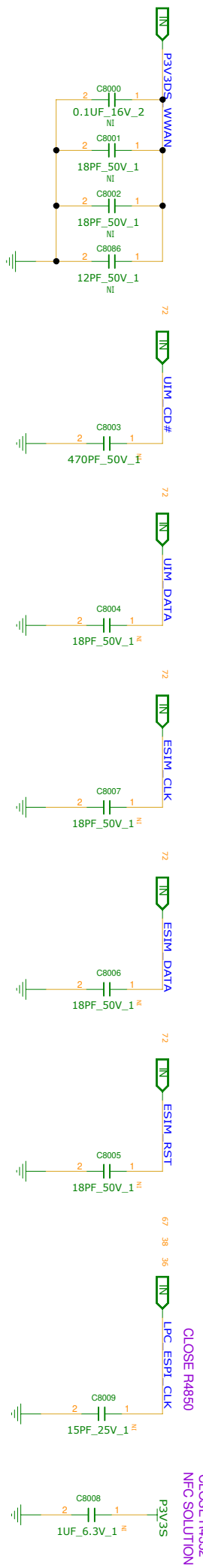


LOCATION NUMER : 8000~8199

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
EMI SOLUTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 81 of 100			

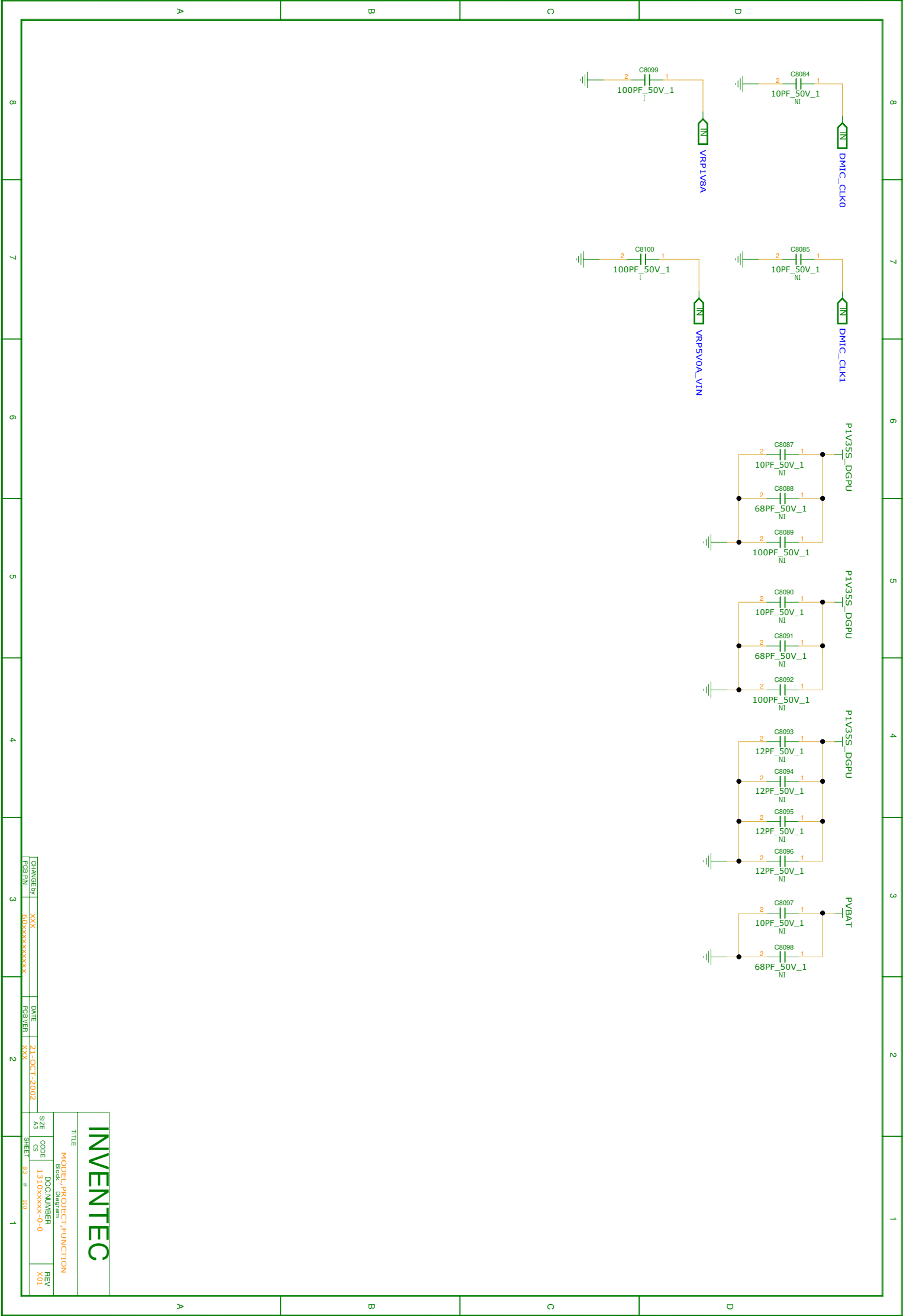
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

RF SOLUTION



LOCATION NUMBER : 8000~8199

INVENTEC			
MODEL/PROJECT/FUNCTION			
NO. DOCUMENT			
SIZE			
REV			
CHANGE BY	XXX	DATE	21-OCT-2002
PCB P/N	60XXXXXXXXXX	PCB VER	XXX
SHEET		82	300



INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
BIOSECURITY			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 83 of 100			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxxx	PCB VER	XXX

D

C

B

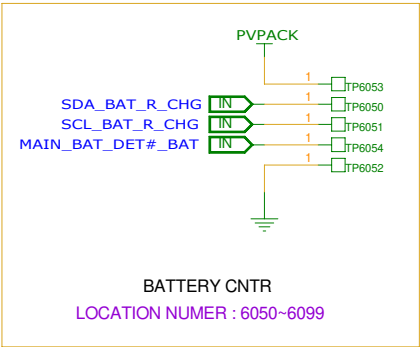
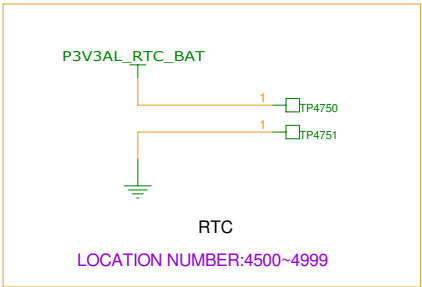
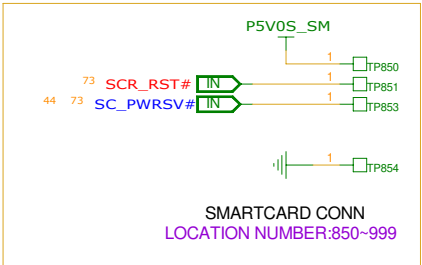
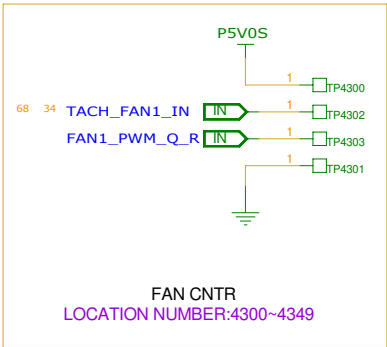
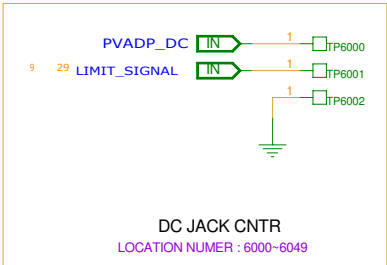
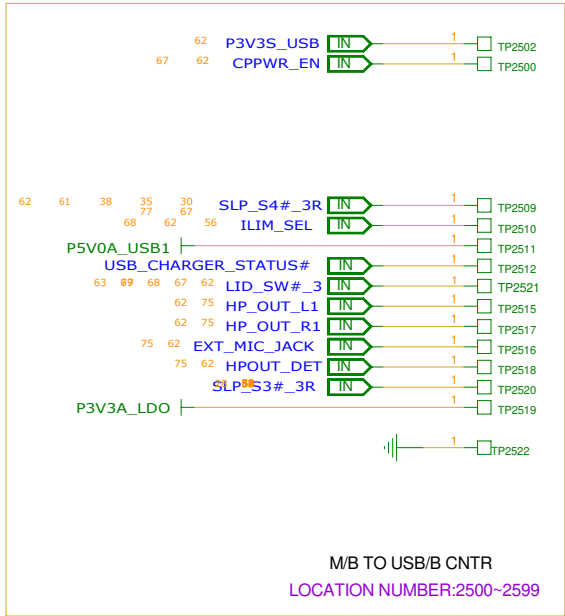
A

D

C

B

A

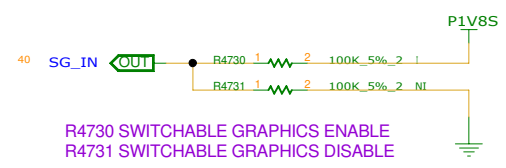
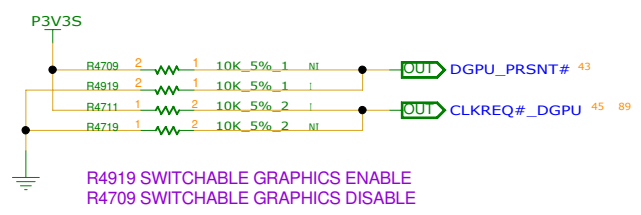


INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
TEST POINT

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 84 of 100			

CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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FOR UMA MODE	
INSTALL	R4709 R4731
UNINSTALL	R4730 R4919

INVENTEC

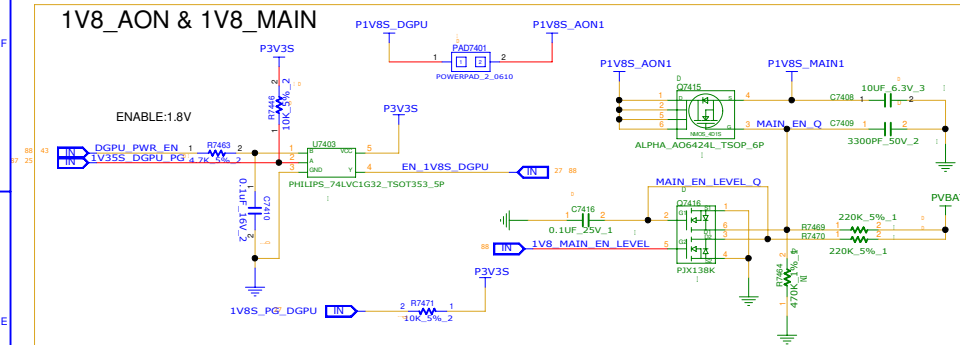
TITLEMODEL PROJECT FUNCTIONDIS & UMA CHANGE PART

SIZEA3CODECSDOC NUMBER1310xxxxx-0-0REVX01

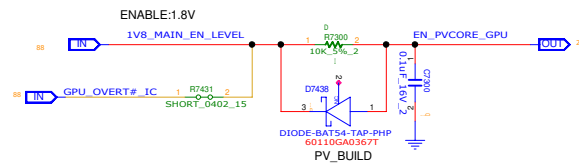
SHEET85 of 100

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

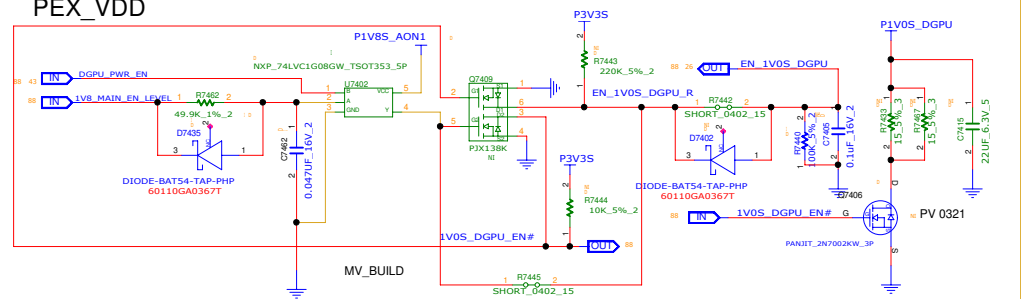
1V8_AON & 1V8_MAIN



NVVDD

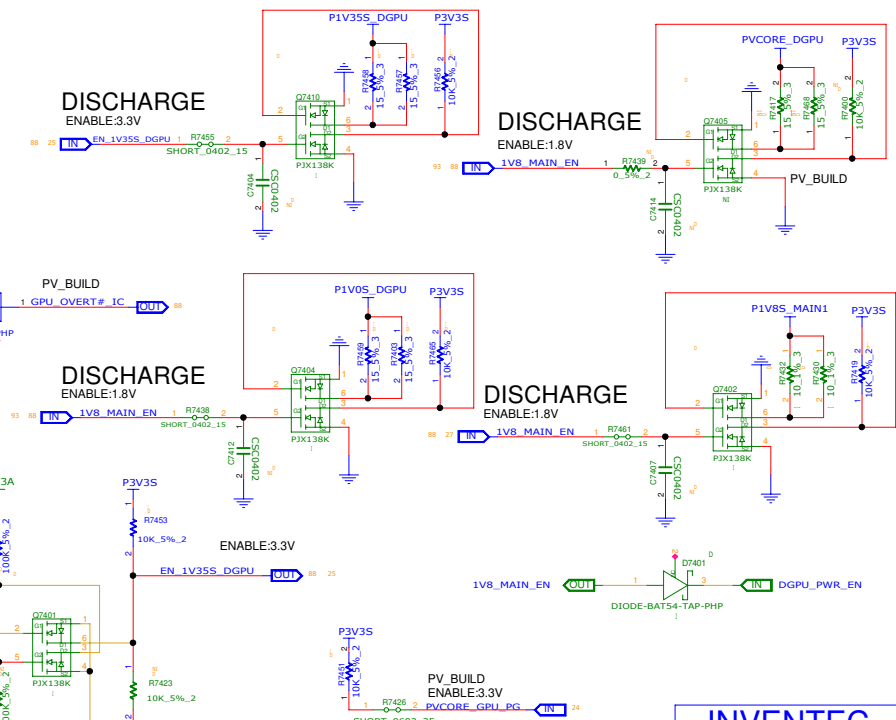
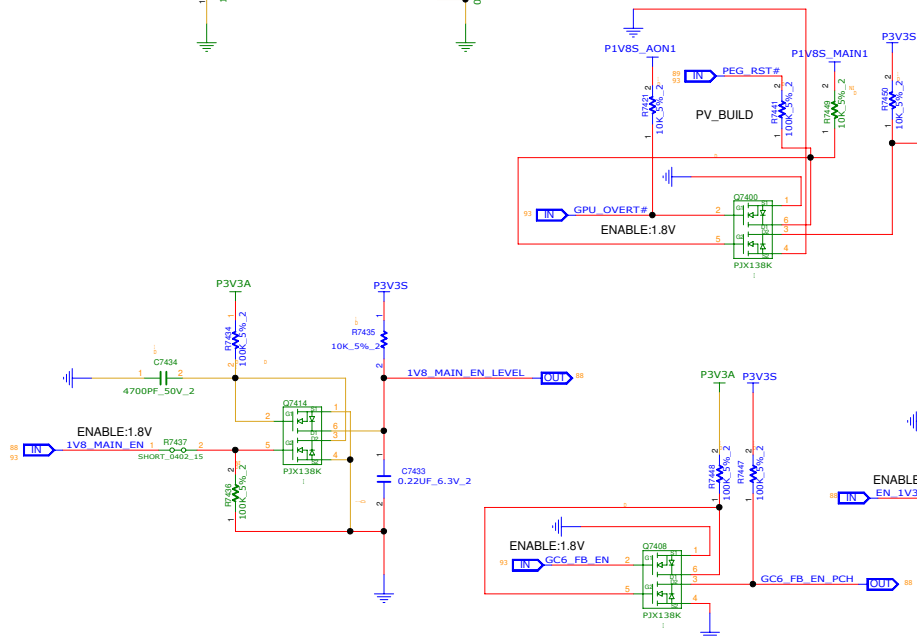
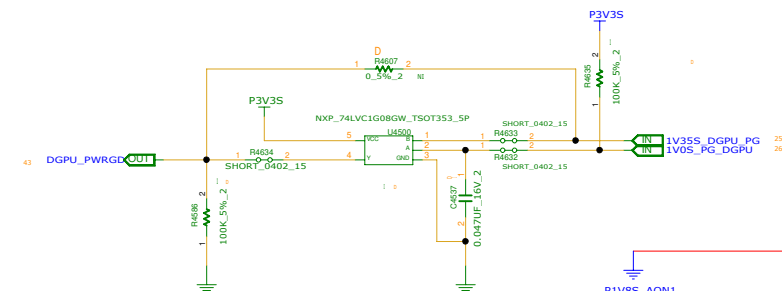
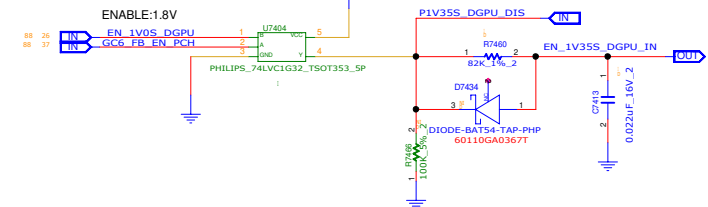


PEX VDD



FBVDD

FOR GC6 2.0



INVENTEC

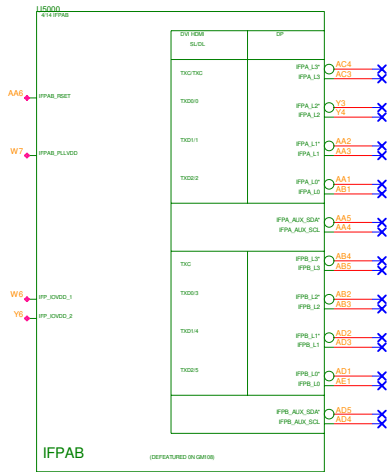
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER		REV
A3	CS	1310xxxxx-0-0		X01
SHEET		86	of 100	



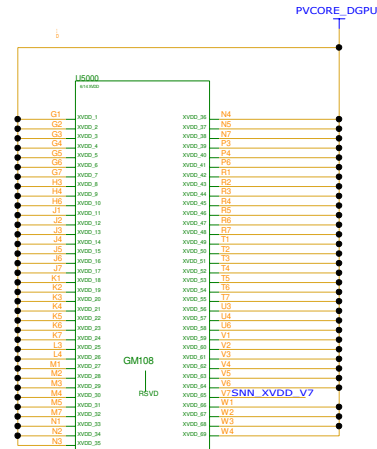
CHANGE b	XXX	DATE	21-OCT-2002
PCB P/N	60XXXXXXXXXX	PCB VER	XXX



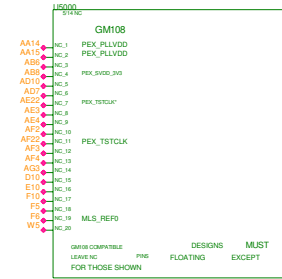




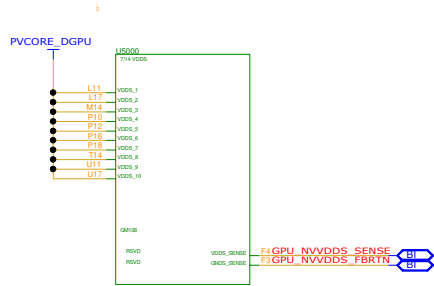
NVIDIA_N17S_G2_A1_FCBGA_595P



NVIDIA_N17S_G2_A1_FCBGA_595P



NVIDIA_N17S_G2_A1_FCBGA_595P

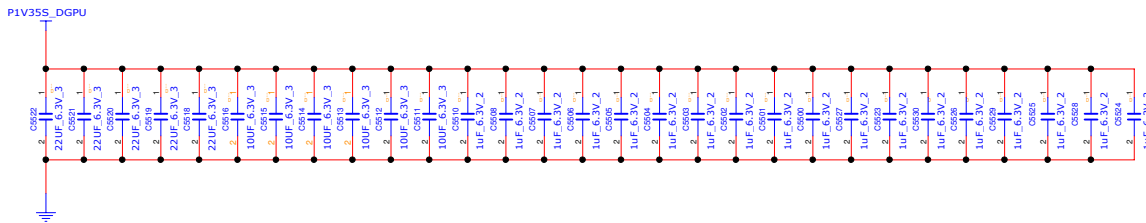
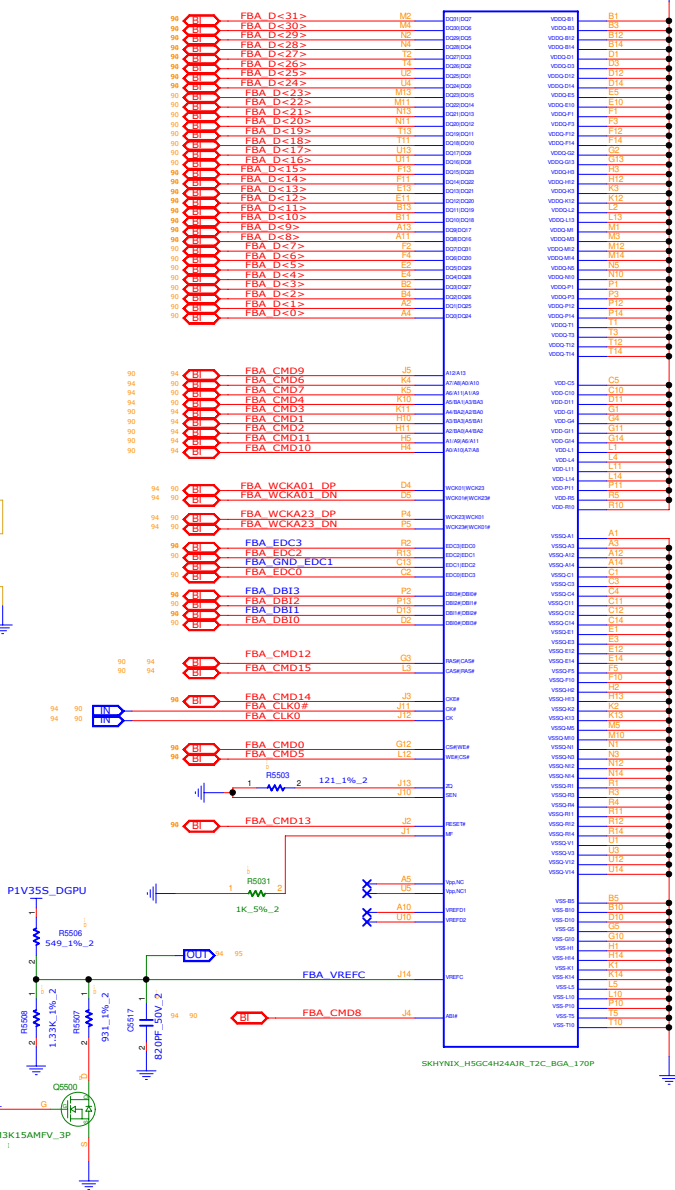
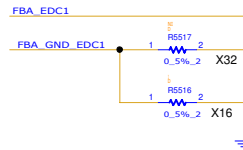


NVIDIA_N17S_G2_A1_FCBGA_595P

NORMAL

U5500
6019B1281401

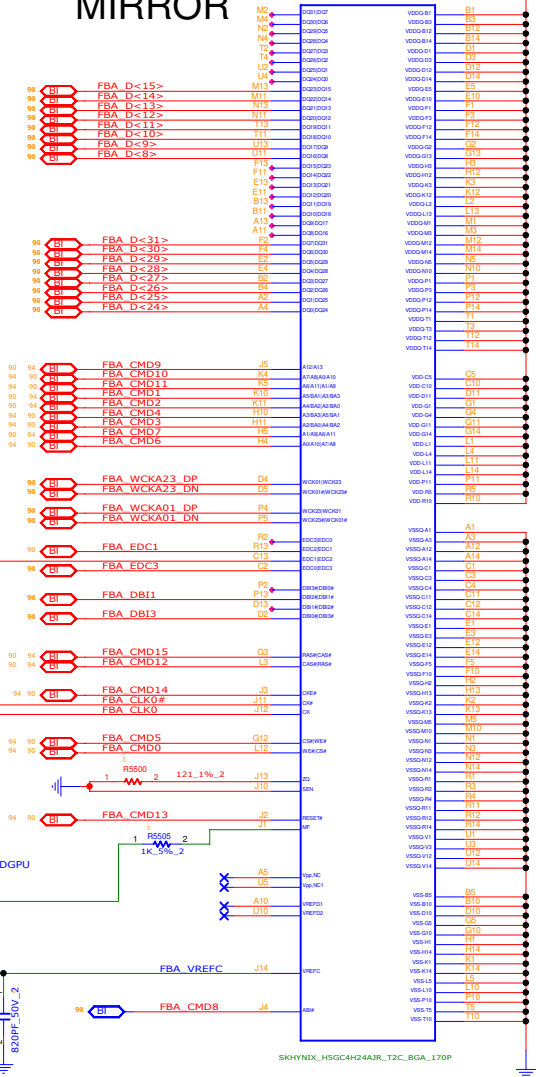
P1V35S_DGPU



MIRROR

U5501

P1V35S DGP



INVENTEC

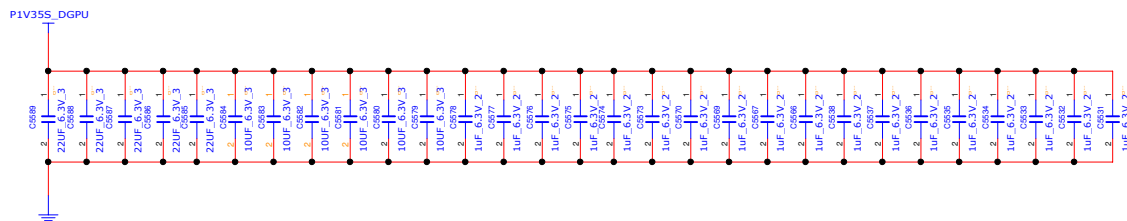
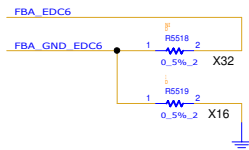
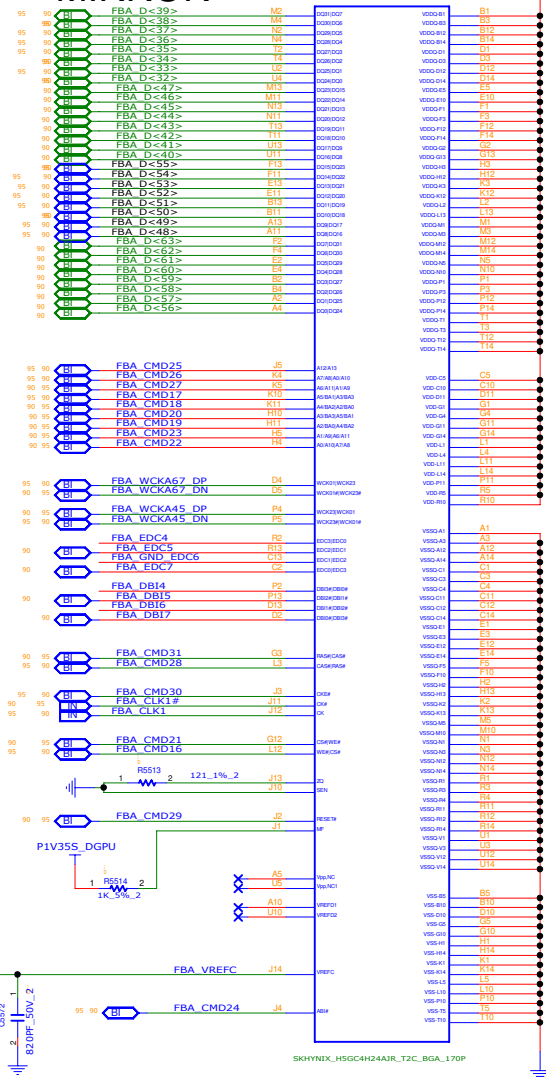
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310xxxxxx-0-0	X01
SHEET		of 92	100

MEMORY: FBA Partition 63..32

MIRROR

U5503

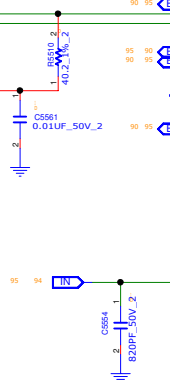
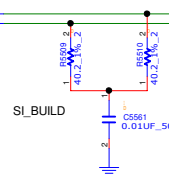
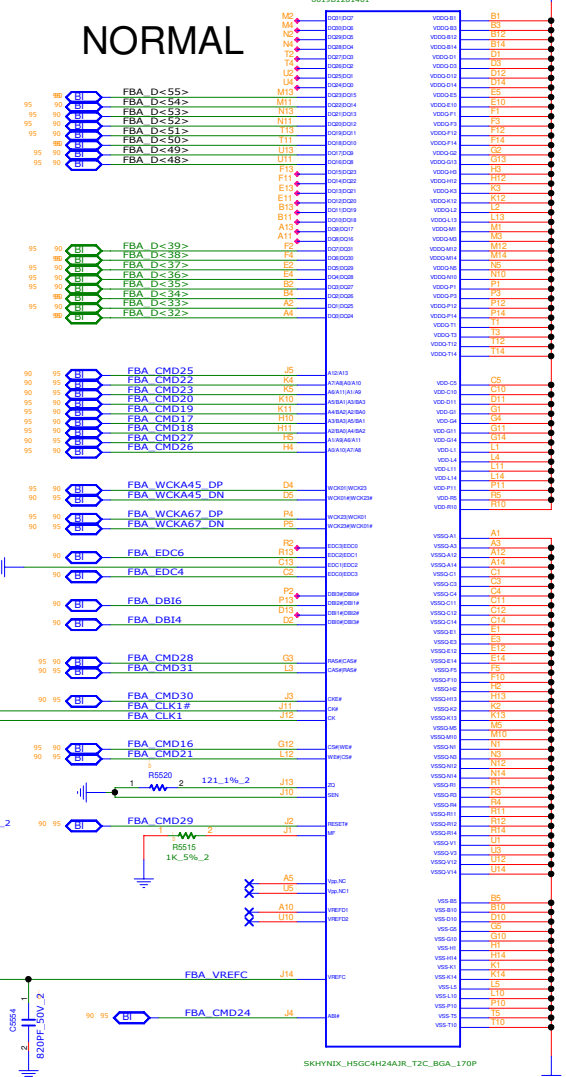
P1V35S_DGPU



NORMAL

U5502

P1V35S DGPU

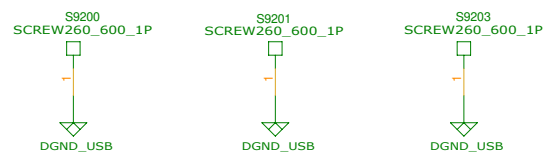
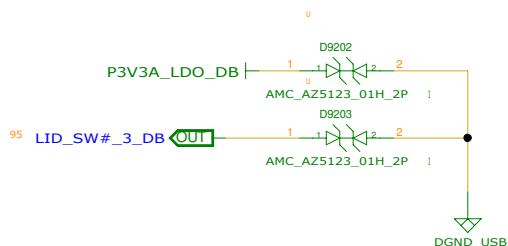
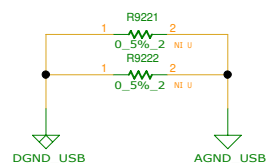
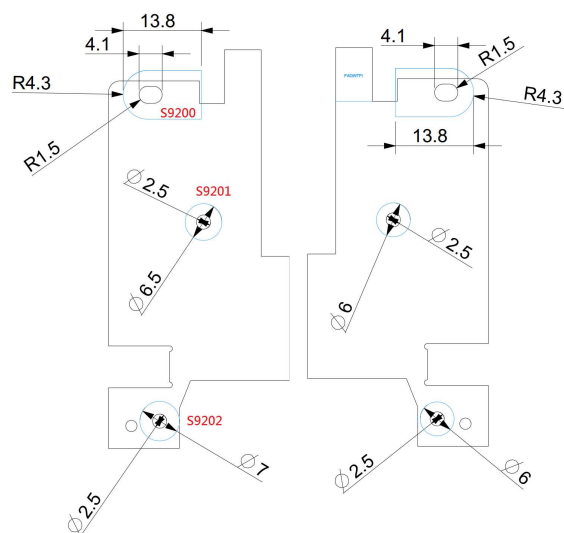


INVENTEC

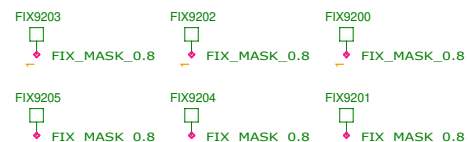
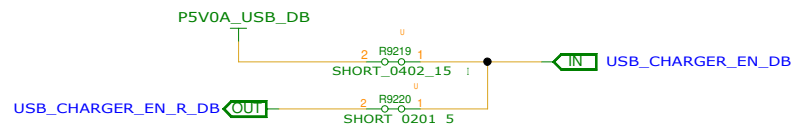
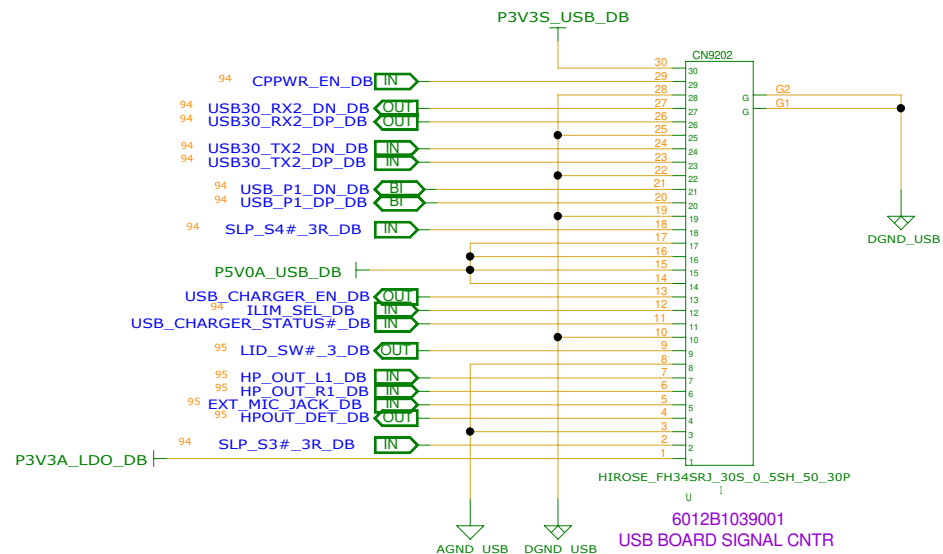
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	
C	CS	1310xxxxx-0-0	
SHEET		of	93 100

CHANGE by	XXX	DATE	
PCB P/N	60x-ENG-XXXXX	PCB VER 31	-OCTY2002

USB & AUDIO JACK & LID SWITCH DB CNTR



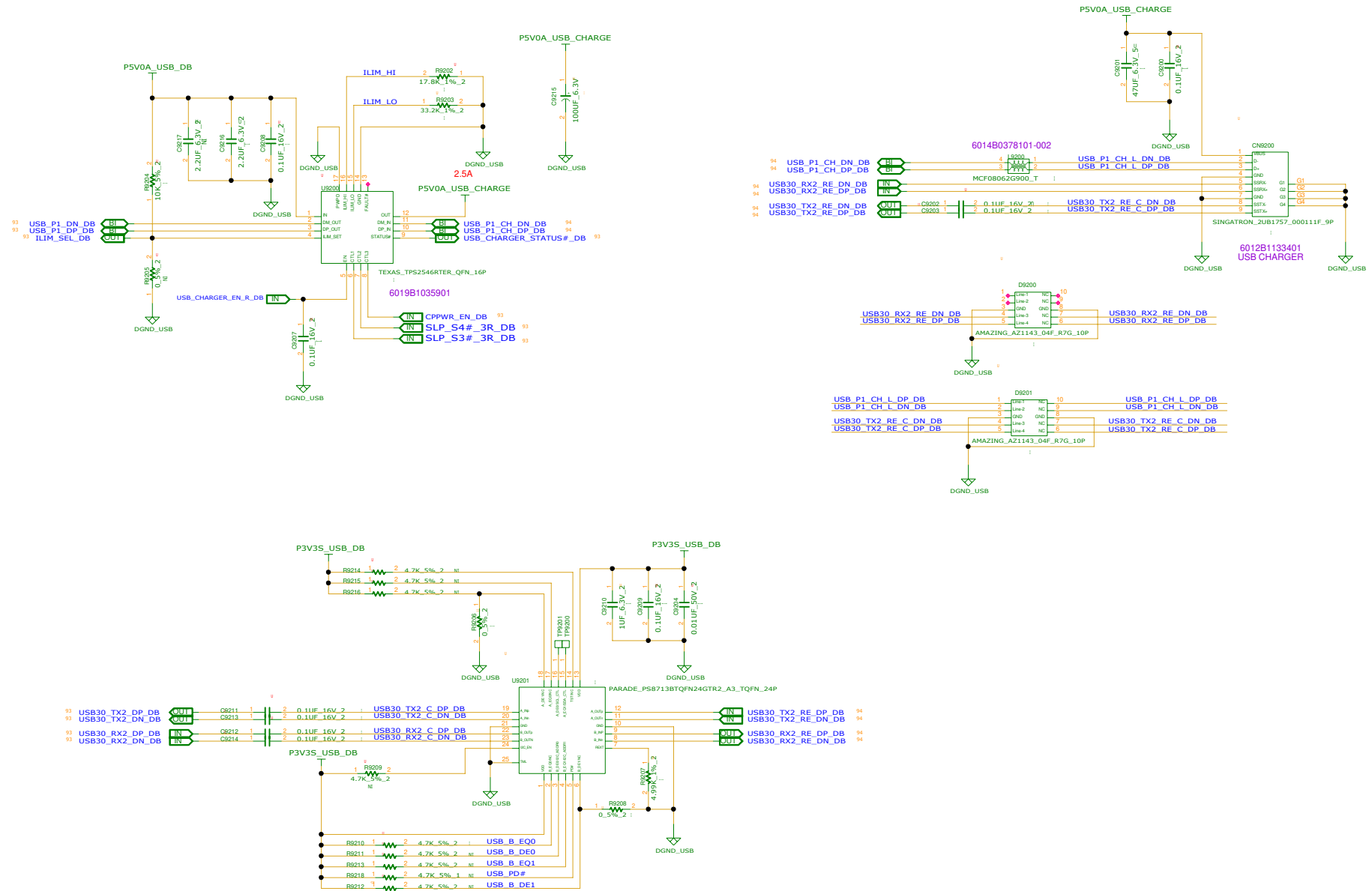
LOCATION NUMBER:9200~9299



INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
USB DB CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 94 of 100			

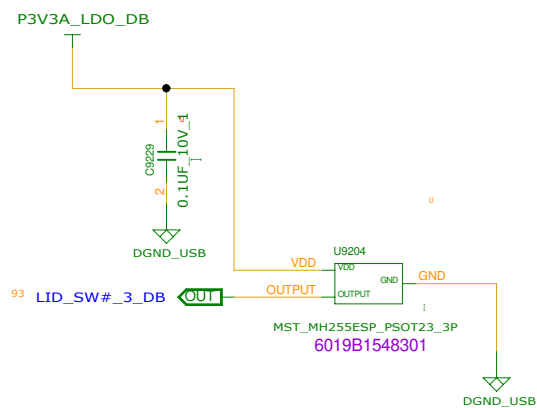
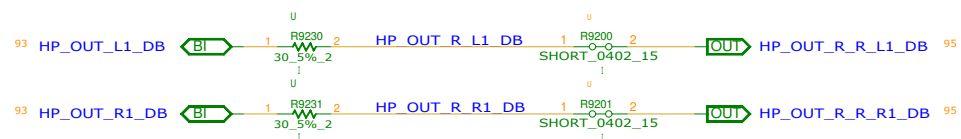
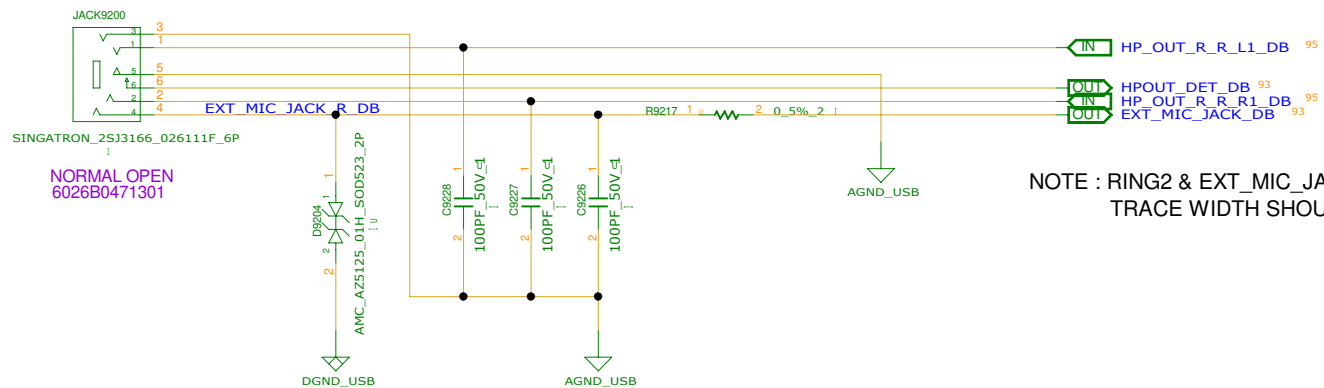
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

USB TYPEA PORT_A



LOCATION NUMBER:9200~9299

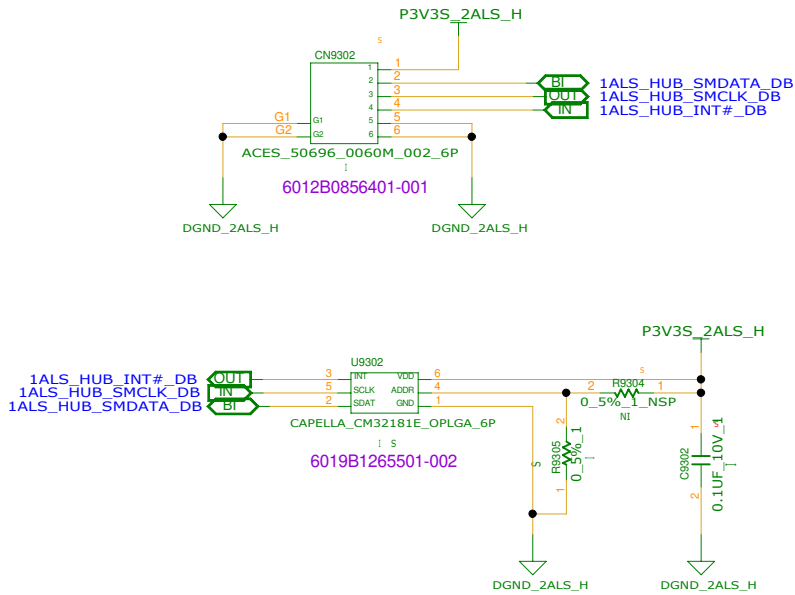
INVENTEC			
TITLE MODEL,PROJECT,FUNCTION USB CHARGER POART			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET 95		of 100	



LOCATION NUMBER:9200~9299

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
AUDIO JACK DB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 96 of 100			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



HUB - AMBIENT LIGHT SENSOR

ADDR IS HIGH, SELECT ADDRESS 0X48
 ADDR IS LOW, SELECT ADDRESS 0X10

LOCATION NUMBER:9300~9349

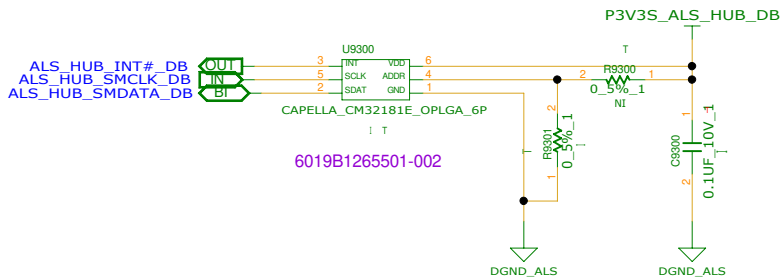
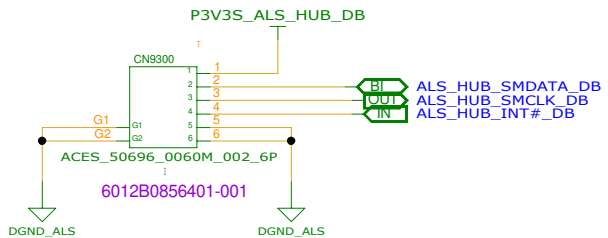
INVENTEC

TITLE MODEL PROJECT,FUNCTION
 ALS D/B

SIZE CODE DOC NUMBER REV
 A3 CS 1310xxxxx-0-0 X01

SHEET 98 of 100

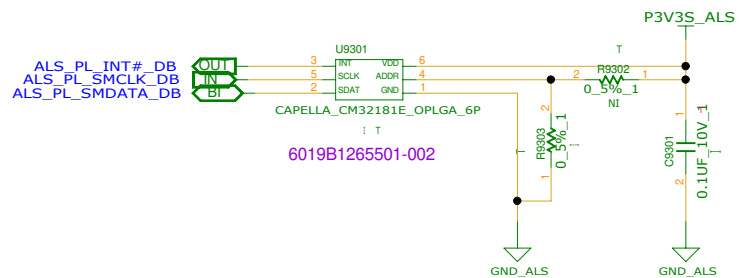
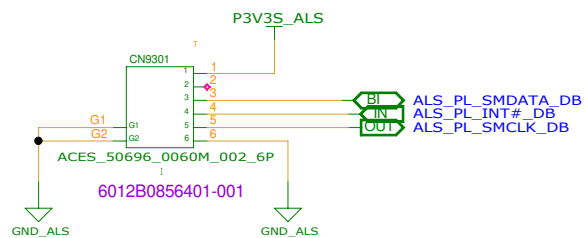
CHANGE by XXX DATE 21-OCT-2002
 PCB P/N 60xxxxxxxxxxx PCB VER XXX



HUB - AMBIENT LIGHT SENSOR

ADDR IS HIGH, SELECT ADDRESS 0X48
ADDR IS LOW, SELECT ADDRESS 0X10

LOCATION NUMBER:9300~9349

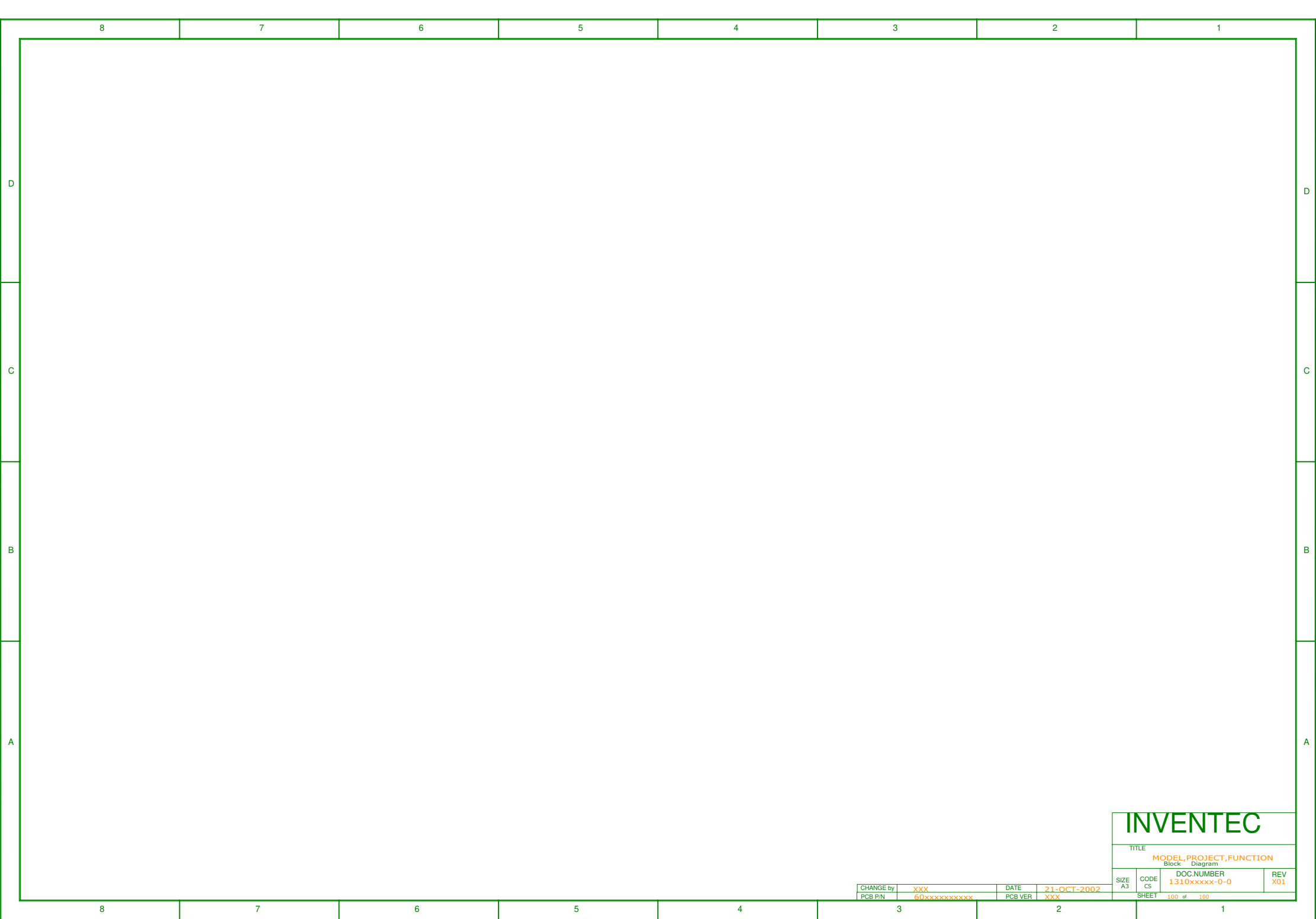


PANEL - AMBIENT LIGHT SENSOR

ADDR IS HIGH, SELECT ADDRESS 0X48
ADDR IS LOW, SELECT ADDRESS 0X10

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
ALS D/B			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 99 of 100			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		100 of 100	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX